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Self-stabilizing Byzantine fault-tolerant repeated reliable broadcast [☆]

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ABSTRACT

We study a well-known communication abstraction called Byzantine Reliable Broadcast (BRB). This abstraction is central in the design and implementation of fault-tolerant distributed systems, as many fault-tolerant distributed applications require communication with provable guarantees on message deliveries. Our study focuses on fault-tolerant implementations for message-passing systems that are prone to process-failures, such as crashes and malicious behavior.

At PODC 1983, Bracha and Toueg, in short, BT, solved the BRB problem. BT has optimal resilience since it can deal with $t < n/3$ Byzantine processes, where n is the number of processes. The present work aims to design an even more robust solution than BT by expanding its fault-model with self-stabilization, a vigorous notion of fault-tolerance. In addition to tolerating Byzantine and communication failures, self-stabilizing systems can recover after the occurrence of *arbitrary transient-faults*. These transient faults allow the model to represent temporary deviations from the assumptions on which the system was originally designed to operate (provided that the algorithm code remains intact).

We propose, to the best of our knowledge, the first self-stabilizing Byzantine fault-tolerant (BFT) solution for repeated BRB in signature-free message-passing systems (that follows BT's problem specifications). Our contribution includes a self-stabilizing variation on BT that solves a single-instance BRB for asynchronous systems. We also consider the problem of recycling instances of single-instance BRB. Our self-stabilizing BFT recycling for time-free systems facilitates the concurrent handling of a predefined number of BRB invocations and, in this way, can serve as the basis for self-stabilizing BFT consensus.

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1. Introduction

Fault-tolerant distributed systems are known to be hard to design and verify. High-level communication primitives can facilitate such complex challenges. These high-level primitives can be based on low-level ones, such as the one that allows processes to send a message to only one other process at a time. Hence, when an algorithm wishes to broadcast message m to all processes, it can send m individually to every other process. Note that if failures occur during this broadcast, it can be the case that only some of the processes have received m . Even in the presence of network-level support for broadcasting or

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Table 1
Glossary.

Notation	Meaning
AMP	a fault model for asynchronous message-passing systems.
ARQ	automatic repeat request.
BAMP	a fault model for Byzantine asynchronous message-passing systems.
BFT	the design criteria of Byzantine fault-tolerant.
BML	a synchrony assumption about bounded message lifetime, λ .
BRB	the problem of Byzantine Reliable Broadcast.
BT	the studied non-self-stabilizing BFT algorithm by Bracha and Toueg [4,5].
FC	the fault model-related assumption about fair communications.
IRC	the problem abstraction of Independent Round Counter, which is used for implementing the proposed BRB-instance recycling for repeated BRB.
RB	the problem of Reliable Broadcast.
SSBFT	self-stabilizing Byzantine fault-tolerant.
n	number of nodes in the system.
t	an upper bound on the number of faulty nodes.
channelCapacity	an upper bound on the number of messages in any given communication channel.
δ	a constant number of concurrent BRB instances.
λ	a bound on the BML lifetime.
$\diamond P_{mute}$	a class of mute failure detectors.

multicasting, node failures can cause similar inconsistencies. To simplify the design of fault-tolerant distributed algorithms, such inconsistencies need to be avoided. Many examples show how fault-tolerant broadcasts can significantly simplify the development of fault-tolerant distributed systems, e.g., State Machine Replication [1] and Set-Constrained Delivery Broadcast [2]. The weakest variant, named *Reliable Broadcast*, lets all non-failing processes agree on the set of delivered messages. This set includes all the messages broadcast by the non-failing processes. Stronger Reliable Broadcast variants specify additional requirements on the delivery order. Such requirements can simplify the design of fault-tolerant distributed consensus, which allows reaching, despite failures, a common decision based on distributed inputs. Reliable broadcast and consensus (as well as message-passing emulation of read/write registers [1]) are closely related problems in the area of distributed computing. This work aims to design a reliable broadcast solution that is more fault-tolerant than the state-of-the-art.

The problem. Lamport, Shostak, and Pease [3] said that a process commits a Byzantine failure if it deviates from the algorithm instructions, say, by deferring (or omitting) messages that were sent by the algorithm or sending fake messages. Such malicious behavior can be the result of hardware malfunctions or software errors as well as coordinated malware attacks. Bracha and Toueg [4,5], BT from now on, proposed the communication abstraction of Byzantine Reliable Broadcast (BRB), which allows every process to invoke the $brbBroadcast(v)$ operation and raise the $brbDeliver()$ event upon message arrival. For convenience, the Glossary is provided in Table 1, see the end of this section. Following Raynal [1, Ch. 4], we consider the (single instance) BRB problem.

Single-instance BRB. Definition 1.1 specifies the task of single-instance BRB.

Definition 1.1. We require $brbBroadcast(v)$ and $brbDeliver()$ to satisfy:

- **BRB-validity.** Suppose a correct process BRB-delivers message m from a correct process p_i , where the term correct node refers to a non-faulty one. Then, p_i had BRB-broadcast m .
- **BRB-integrity.** No correct process BRB-delivers more than once.
- **BRB-no-duplcity.** No two correct processes BRB-deliver different messages from p_i (which might be faulty).
- **BRB-Completion-1.** Suppose p_i is a correct sender. All correct processes BRB-deliver from p_i eventually.
- **BRB-Completion-2.** Suppose a correct process BRB-delivers a message from p_i (who might be faulty). All correct processes BRB-deliver p_i 's message eventually.

Note that self-stabilizing systems cannot be quiescent [6, Ch. 2.3]. In that sense, once the completion properties are satisfied, the self-stabilizing system cannot stop sending messages. For this reason, we use the name completion when referring to these properties rather than the common name in the literature, e.g., [1, Ch. 4], which is termination.

Repeated BRB. Distributed systems may use, over time, an unbounded number of BRB instances. We require our solution to use, at any given point in time, a bounded amount of memory. Thus, for the sake of completeness, we also consider the problem of recycling an unbounded sequence of BRB invocations using bounded memory. We require the (single-instance)

BRB object, O , to have an operation, called `recycle()`, that allows the recycling mechanism to locally reset O , after all non-faulty processes have completed the delivery of O 's message. Also, we require the mechanism to inform (the possibly recycled) O regarding its availability to take new missions. Specifically, the `txAvailable()` operation returns `True` when the sender can use O for broadcasting, and `rxAvailable(k)` returns `True` when O 's latest transmission has arrived at the receiver p_k .

One may observe that the problem statement does not depend on the fault model or the design criteria. However, the proposed solution depends on all three. To clarify, we solve the single instance BRB using the requirements presented by Raynal [1, Ch. 4]. Then, we solve an extended version of the problem in which each BRB instance needs to be recycled so that an unbounded number of BRB instances can appear.

Fault models. Recall that our BRB solution may be a component in a system that solves consensus. Thus, we safeguard against Byzantine failures by following the same assumptions that are often used when solving consensus. Specifically, for the sake of deterministic and signature-free solvability [7], we assume there are at most $t < n/3$ crashed or Byzantine processes, where n is the total number of processes. The proposed solutions are for message-passing systems that have no guarantees on the communication delay and without explicit access to the clock. These systems are also prone to communication failures, e.g., packet omission and duplication, as long as fair communication (FC) holds, i.e., if p_i sends a message infinitely often to p_j , then p_j receives that message infinitely often. An extension of our solution can also deal with packet reordering via the use of the automatic repeat request (ARQ) solution presented in [8], cf. § 6.) We use two different fault models with notations following Raynal [1]:

- $\text{BAMP}_{n,t}[\text{FC}, t < n/3]$. This is a Byzantine Asynchronous Message-Passing model with at most t (out of n) faulty nodes. The array $[\text{FC}, t < n/3]$ denotes the list of all assumptions, i.e., FC and $t < n/3$. We use this model for studying the problem of single-instance BRB since it has no synchrony assumptions.
- $\text{BAMP}_{n,t}[\text{FC}, t < n/3, \diamond P_{\text{mute}}, \text{BML}]$. By Doudou et al. [9], processes commit muteness failures when they stop sending specific messages, but they may continue to send "I-am-alive" messages. For studying the problem of BRB instance recycling, we enrich $\text{BAMP}_{n,t}[\text{FC}, t < n/3]$ with a muteness detector of class $\diamond P_{\text{mute}}$ and assume bounded message lifetime (BML). That is, in any unbounded sequence of BRB invocations, at the time that immediately follows the x -th invocation, the messages associated with the $(x-\lambda)$ -th invocation (or earlier) are either delivered or lost, where λ is a known upper-bound.

Raynal [1] refers to an asynchronous system as *time-free* when it includes synchrony assumptions, e.g., BML. Note that BML does not imply bounded communication delay since an unbounded number of messages can be lost between any two successful transmissions. Our time-free implementation of the class $\diamond P_{\text{mute}}$ muteness detector uses an additional synchrony assumption, i.e., there is a known bound, Θ , on the number of messages that some non-faulty processes can exchange without hearing from all non-faulty processes.

Self-stabilization. Dijkstra's seminal work [10] demonstrated recovery within a finite time after the occurrence of the last transient fault, which may corrupt the system state in any manner (as long as the program code stays intact). Dijkstra offered an alternative to traditional fault-tolerance, which aims at assuring that the system, at all times, remains in a correct state under the assumption that the system state changes only due to the algorithmic steps and specified failures. Alas, the latter target is unattainable in the presence of failures that were unforeseen during the algorithm design. To address this concern, self-stabilization considers failures that are transient by nature and hard to be observed. Thus, they cannot be specified by the fault model, such as the one above, which includes process and communication failures. Therefore, self-stabilizing systems are required to recover eventually (in the presence of all foreseen and specified failures) after the occurrence of the last unforeseen and transient failure.

In this paper, in addition to the faults specified above, we also aim to recover after the occurrence of the last *arbitrary transient-fault* [11,6]. These transient faults allow the model to represent temporary deviations from the assumptions on which the system was originally designed to operate. This includes the corruption of control variables, such as the program counter and packet payloads, as well as operational assumptions, such as that at most $t < n/3$ processes are faulty. Since the occurrence of these failures can be arbitrarily combined, we assume these transient-faults can alter the system state in unpredictable ways. When modeling the system, Dijkstra assumed that these violations can bring the system to an arbitrary state from which a *self-stabilizing system* should recover [10]. I.e., Dijkstra requires the correctness proof of a self-stabilizing system to demonstrate recovery within a finite time after the last occurrence of a transient-fault and once the system has recovered, it must never violate the task requirements since no further transient faults occur. Arora and Gouda [12] refer to the former property as Convergence and the latter as Closure. Note that the satisfaction of the task requirements, which is Definition 1.1 in the case of this paper, holds only when Closure is guaranteed. In other words, only after the system has finished recovering from the occurrence of the last transient fault does a self-stabilizing system guarantees the satisfaction of the task requirements, for details see [11,6].

Related work. In the context of Reliable Broadcast, there are (non-self-stabilizing) Byzantine fault-tolerant (BFT) solutions [1, 13,14] and (non-BFT) self-stabilizing solutions [15] (even for total order broadcast [15–18]). We focus on BT [4,5] to which we propose a self-stabilizing variation. BT is the basis for advanced BFT algorithms for solving consensus, such as the one

by Mostéfaoui and Raynal [19]. BT is based on a simpler communication abstraction by Toueg that is called no-duplicity broadcast [20] (ND-broadcast, from now on). The requirements of ND-broadcast include all of Definition 1.1's requirements except for BRB-Completion-2.

Maurer and Tixeuil [21] consider an abstraction that is somewhat simpler than the one of ND-broadcast since they only consider no-duplicity (and none of the other requirements of Definition 1.1). They provide a single-instance synchronous broadcast that is self-stabilizing BFT (or SSBFT in short), whereas we consider an asynchronous repeated BRB that follows Definition 1.1, which is taken from Raynal [1]. Raynal studies the exact power of all the essential communication abstractions in the area of fault-tolerant message-passing systems. We study the more versatile definition provided by Raynal since we wish to connect our solution to all relevant protocols in the area.

In the broader context of SSBFT solutions for message-passing systems, we find topology discovery [22], storage [23–25], clock synchronization [26,27], approximate agreement [28], asynchronous unison [29], communication in dynamic networks [30], and SSBFT state-machine replication [31,32] to name a few. We also propose an SSBFT mechanism for recycling single-instance BRB objects, which uses a muteness detector inspired by Doudou et al. [9]. Observe that Doudou et al. consider the Consensus problem, whereas we consider BRB.

Our contribution. We present SSBRB, a fundamental module for dependable distributed systems: a Self-Stabilizing BFT Reliable Broadcast for asynchronous message-passing systems, *i.e.*, for the model of $\text{BAMP}_{n,t}[\text{FC}, t < n/3]$. We obtain this new self-stabilizing solution via a transformation of the non-self-stabilizing BT algorithm [4,5] while preserving BT's resilience optimality of $t < n/3$. As in BT [4,5], in the absence of transient-faults, our asynchronous solution for single-instance BRB achieves operation completion within a finite time. After the occurrence of the last transient-fault, the system recovers within a bounded time in terms of asynchronous cycles (while assuming execution fairness among the non-faulty processes). The amount of memory used by the proposed algorithm is bounded and the communication costs of the studied and proposed algorithms are similar, *i.e.*, $\mathcal{O}(n^2)$ messages per BRB instance. The main difference is that our solution unifies all the types of messages sent by BT into a single message that is repeatedly sent. This repetition is imperative since self-stabilizing systems cannot be quiescent [6, Ch. 2.3].

Our contribution also includes an SSBFT recycling mechanism for time-free systems that are enriched with muteness detectors, *i.e.*, for the $\text{BAMP}_{n,t}[\text{FC}, \text{BML}, \diamond\text{P}_{\text{mute}}]$ model. The mechanism is based on an algorithm that counts communication rounds. Since individual BRB broadcasters increment the counter independently, the algorithm is named the *Independent Round Counter* (IRC) algorithm. Implementing an SSBFT IRC is a non-trivial challenge since this counter should facilitate an unbounded number of increments, yet it has to use only a constant amount of memory. Using novel techniques for dealing with integer overflow events, the proposed solution recovers from transient faults eventually, uses a bounded amount of memory, and has a communication cost of $\mathcal{O}(n)$ messages per BRB instance.

To the best of our knowledge, we propose the first SSBFT solutions for the problems of IRC and repeated BRB (that follows BT's problem specifications [1, Ch. 4]). As said, BRB and IRC consider different fault models. § 2 defines $\text{BAMP}_{n,t}[\text{FC}, t < n/3]$ and self-stabilization. The non-self-stabilizing BT algorithm for $\text{BAMP}_{n,t}[\text{FC}, t < n/3]$ is studied in § 3. Our self-stabilization BFT variation on BT for $\text{BAMP}_{n,t}[\text{FC}, t < n/3]$ is proposed in § 4. Our SSBFT IRC for $\text{BAMP}_{n,t}[\text{FC}, t < n/3, \diamond\text{P}_{\text{mute}}, \text{BML}]$ is presented in § 6. § 7 presents the correct proof of the proposed SSBFT IRC. § 9 compares the overhead of the studied and proposed solutions when executing δ BRB instances concurrently. This straightforward extension is imperative for the sake of practical deployments.

Recall that our task specifications (Definition 1.1) follow the ones by Raynal [1, Ch. 4], and thus, our SSBRB solution can serve as a building block for multivalued consensus [33].

2. System settings for $\text{BAMP}_{n,t}[\text{FC}, t < n/3]$

This work focuses on two fault models. The asynchronous model for message-passing systems, $\text{BAMP}_{n,t}[\text{FC}, t < n/3]$, is presented in this section. This model has a time-free enrichment, $\text{BAMP}_{n,t}[\text{FC}, t < n/3, \diamond\text{P}_{\text{mute}}, \text{BML}]$, which we explain in § 6. The $\text{BAMP}_{n,t}[\text{FC}, t < n/3]$ model requires no guarantees of communication delay. Also, the algorithm cannot explicitly access the (local) clock (or use timeout mechanisms). The system consists of a set, \mathcal{P} , of n fail-prone nodes (or processes) with unique identifiers. Any pair of nodes $p_i, p_j \in \mathcal{P}$ have access to a bidirectional communication FIFO-channel, $\text{channel}_{j,i}$, that, at any time, has at most $\text{channelCapacity} \in \mathbb{Z}^+$ messages on transit from p_j to p_i (this assumption is due to a known impossibility [6, Ch. 3.2]).

In the *interleaving model* [6], the node's program is a sequence of (*atomic*) steps. Each step starts with an internal computation and finishes with a single communication operation, *i.e.*, a message *send* or *receive*. The state, s_i , of node $p_i \in \mathcal{P}$ includes all of p_i 's variables and all incoming communication FIFO-channels, $\text{channel}_{j,i} : p_i, p_j \in \mathcal{P}$. The term *system state* (or configuration) refers to the tuple $c = (s_1, s_2, \dots, s_n)$. We define an *execution* (or run) $R = c[0], a[0], c[1], a[1], \dots$ as an alternating sequence of system states $c[x]$ and steps $a[x]$, such that each $c[x+1]$, except for the starting one, $c[0]$, is obtained from $c[x]$ by $a[x]$'s execution.

The fault model and self-stabilization. The *legal executions* (*LE*) set refers to all the executions in which the requirements of task T hold. In this work, T_{BRB} denotes the task of BFT Reliable Broadcast, which § 1 specifies, and the executions in the set LE_{BRB} fulfill T_{BRB} 's requirements.

Benign failures. When the failure occurrence cannot cause the execution to lose legality, *i.e.*, to leave LE , we refer to that failure as a benign one.

- *Communication failures and fairness.* We focus on solutions that are oriented towards asynchronous message-passing systems and thus they are oblivious to the time at which the packets depart and arrive. We assume that any message can reside in a communication channel only for a finite period. Also, the communication channels are prone to packet failures, such as loss and duplication. However, if p_i sends a message infinitely often to p_j , node p_j receives that message infinitely often. We refer to the latter as the *fair communication* (FC) assumption. As in [34], we assume that the communication channel from a correct node eventually includes only messages that were transmitted by the sender. The BT algorithm assumes reliable communication channels whereas the proposed solution does not make any assumption regarding reliable communications. § 4 provides further details regarding the reasons why the proposed solution cannot make this assumption.

- *Arbitrary node failures.* Byzantine faults model any failure in a node including crashes, and arbitrary malicious behaviors. Here, all nodes receive the arriving messages and calculate their state according to the algorithm. However, once a node, that is captured by the adversary, sends a message, the adversary can modify the message in any way, delay it for an arbitrarily long period or even omit it from the communication channel. The adversary can also send fake messages, *i.e.*, not according to the algorithm. Note that the adversary has the power to coordinate such actions without any computational (or communication) limitation. For the sake of solvability [3,7,20], the fault model that we consider limits only the number of nodes that can be captured by the adversary. That is, the number, t , of Byzantine failures needs to be less than one-third of the number, n , of nodes in the system, *i.e.*, $3t + 1 \leq n$. The set of non-faulty indexes is denoted by *Correct*, such that $i \in \text{Correct}$ when p_i is a correct node.

Arbitrary transient-faults. We consider any temporary violation of the assumptions according to which the system was designed to operate. We refer to these violations and deviations as *arbitrary transient-faults* and assume that they can corrupt the system state arbitrarily (while keeping the program code intact). The occurrence of a transient fault is rare. Thus, we assume that the last arbitrary transient fault occurs before the system execution starts [6]. Also, it leaves the system to start in an arbitrary state.

Dijkstra's self-stabilization. An algorithm is *self-stabilizing w.r.t. LE*, when every execution R of the algorithm reaches within a finite period a suffix $R_{\text{legal}} \in LE$ that is legal. Namely, Dijkstra [10] requires $\forall R : \exists R' : R = R' \circ R_{\text{legal}} \wedge R_{\text{legal}} \in LE \wedge |R'| \in Z^+$, where the operator \circ denotes that $R = R' \circ R''$ is the concatenation of R' with R'' . By Arora and Gouda [12], the part of the proof that shows the existence of R' is called the *Convergence* (or *recovery*) proof, and the part that shows that $R_{\text{legal}} \in LE$ is called the *Closure* proof.

Complexity measures. The time between the invocation of a BRB-broadcast and the occurrence of all required BRB-deliveries is called latency. As in MR [35], we show finite latency without assuming execution fairness, *i.e.*, every correct node that has an applicable step infinitely often, takes a step infinitely often.

Stabilization time refers to the period in which the system recovers after the occurrence of the last transient fault. When estimating the stabilization time, our analysis assumes that all correct nodes complete roundtrips infinitely often with all other correct nodes, where a roundtrip is defined as the successful exchange of request-reply messages. However, once the Convergence period is over, no fairness assumption is needed.

The stabilization time is measured in terms of asynchronous cycles. The definition of these cycles uses the constant number num_b , which is the maximum number of broadcasts per iteration of the 'do forever loop'. Note that all self-stabilizing algorithms have such 'do forever loop' since these systems cannot be quiescent due to a well-known impossibility [6, Ch. 2.3].

The first asynchronous cycle, R' , of execution $R = R' \circ R''$ is the shortest prefix of R in which every correct node can start and complete at least num_b roundtrips with every correct node. The second asynchronous communication round of R is the first round of the suffix R'' , and so on.

3. The non-self-stabilizing BT algorithm

Recall that the BT algorithm [4,5], is a BRB solution for $\text{BAMP}_{n,t}[\text{FC}, t < n/3]$. BT is based on the simpler communication abstraction, ND-broadcast. The ND-broadcast's task includes all of the BRB requirements (§ 1) except BRB-Completion-2. We review BT after studying the ND-broadcast algorithm [20].

ND-Broadcast. Algorithm 1 brings Toueg's solution [20]. It assumes that every correct node invokes ND-broadcast at most once. Node p_i initiates the ND-broadcasts of m_i by sending $\text{INIT}(m_i)$ to all nodes (line 1). Upon the first arrival of this message to p_j , it disseminates the fact that p_i has initiated m_i 's ND-broadcast by sending $\text{ECHO}(i, m)$ to all nodes (line 2). In the code of the algorithm, when message $\text{INIT}(m)$ arrives from p_j , the handling procedure passes the parameter m_j to allow access to the field $m.j$. Upon its arrival to p_k from more than $(n+t)/2$ different nodes, p_k ND-deliver $\langle i, m_i \rangle$ (line 3). Please note that in lines 3 to 5, the event handler procedure is defined with two parameters: k and $m.k$. These parameter names adhere to a convention where p_k 's message is represented as $m.k$.

Byzantine Reliable Broadcast. As explained, we present the BT solution for BRB as an extension of Toueg's solution for ND-broadcast. Algorithm 2 satisfies the BRB requirements (§ 1) assuming $t < n/3$.

Algorithm 1: Non-self-stabilizing no-duplicity broadcast (ND-broadcast); code for p_i .

```

1 operation ndBroadcast( $m$ ) do broadcast INIT( $m$ );
2 upon INIT( $m$ ) first arrival from  $p_j$  do broadcast ECHO( $j, m$ );
3 upon ECHO( $k, mK$ ) arrival from  $p_j$  begin
4   if ECHO( $k, mK$ ) received from at least  $(n+t)/2$  nodes then
5     ndDeliver( $k, mK$ )

```

Algorithm 2: Non-self-stabilizing Byzantine Reliable Broadcast (BRB); code for p_i .

```

1 operation brbBroadcast( $m$ ) do broadcast INIT( $m$ );
2 upon INIT( $m$ ) first arrival from  $p_j$  do broadcast ECHO( $j, m$ );
3 upon ECHO( $k, mK$ ) arrival from  $p_j$  begin
4   if ECHO( $k, mK$ ) received from at least  $(n+t)/2$  nodes  $\wedge$  READY( $k, mK$ ) not yet broadcast then broadcast READY( $k, mK$ ) ;
5 upon READY( $k, mK$ ) arrival from  $p_j$  begin
6   if READY( $k, mK$ ) received from  $(t+1)$  nodes  $\wedge$  READY( $k, mK$ ) not yet broadcast then broadcast READY( $k, mK$ );
7   if READY( $k, mK$ ) received from at least  $(2t+1)$  nodes  $\wedge$   $(k, mK)$  not yet BRB-Delivered then brbDeliver ( $k, mK$ );

```

The first difference between the ND-broadcast and BRB algorithms is in the consequent clause of the if-statement in line 4, where ND-delivery of $\langle j, m \rangle$ is replaced with the broadcast of READY(j, m). This broadcast indicates that p_i is ready to BRB-deliver $\langle j, m \rangle$ as soon as it receives sufficient support, i.e., the arrival of READY(j, m), which tells that $\langle j, m \rangle$ can be BRB-delivered. Note that BRB-no-duplicity protects Algorithm 2 from the case in which p_i broadcasts READY(j, m) while p_j broadcasts READY(j, m'), such that $m \neq m'$.

The new part of the BRB algorithm (lines 5 to 7) includes two if-statements. The first one (line 6) makes sure that every correct node receives READY(j, m) from at least one correct node before BRB-delivering $\langle j, m \rangle$. This is done via the broadcasting of READY(j, m) as soon as p_i received it, for the first time, from at least $(t+1)$ different nodes (since t of them can be Byzantine).

The second if-statement (line 7) makes sure that no two correct nodes BRB-deliver different pairs (in the presence of plausibly fake READY($j, -$) messages sent by Byzantine nodes, where the symbol '-' stands for any legal value). That is, the delivery of a BRB-broadcast is done only after the first reception of the pair $\langle j, m \rangle$ from at least $(2t+1)$ nodes (out of which at most t are Byzantine). The receiver then knows that there are at least $t+1$ correct nodes that can make sure that the condition in line 6 holds eventually for all correct nodes whenever reliable communication channels are assumed.

4. Self-stabilizing Byzantine-tolerant single-instance BRB

Before proposing our solution, we review the challenges of transforming the non-self-stabilizing BT into a self-stabilizing one.

Challenges and approaches. We analyze here the behavior of the BT algorithm in the presence of transient-faults. We would like to highlight to the reader that our analysis is relevant only in the context of self-stabilization since Bracha and Toueg [4,5] do not consider transient-faults in their work. In Section 8, we explain how to coordinate the recycling of BRB objects.

Dealing with memory corruption and desynchronized system states. Recall that transient faults can corrupt the system state in any manner (as long as the program code remains intact). For example, memory corruption can cause the local state to indicate that a certain message has already arrived (line 2) or that a certain broadcast was already performed (line 6). This means that some necessary messages will not be broadcast. This will result in indefinite blocking. Our solution avoids such situations by unifying all messages and repeatedly broadcasting the unified message (until the BRB object is recycled).

Datagram-based end-to-end communications. Algorithm 2 assumes reliable communication channels when broadcasting in a quorum-based manner, i.e., sending the same message to all nodes and then waiting for a reply from $n-t$ nodes. Next, we explain why, for the sake of a simpler presentation, we choose not to follow this assumption. Self-stabilizing end-to-end communications require a known bound on the capacity of the communication channels [6, Ch. 3]. In the context of self-stabilization and quorum systems, we must avoid situations in which communicating in a quorum-based manner can lead to a contradiction with the system's assumptions. Dolev, Petig, and Schiller [36] explain that there might be a subset of nodes that can complete many roundtrips with a given sender, while other nodes merely accumulate messages in their communication channels. The channel-bounded capacity implies that the system has to either block or omit messages before their delivery. For this reason, the proposed solution does not assume access to reliable channels. Instead, communications are simply repeated by the algorithm's do-forever loop.

Algorithm 3: SSBFT BRB with instance recycling interface; p_i 's code.

```

1 types: brbMSG := {init, echo, ready};
2 variables:
3  $msg[\mathcal{P}][brbMSG] := [[\emptyset, \dots, \emptyset], \dots, [\emptyset, \dots, \emptyset]]$ ; // most recently sent/received message
4  $wasDelivered[\mathcal{P}] := [False, \dots, False]$ ; /* indicates if the message was delivered */
5 required interfaces: txAvailable() and rxAvailable(k);
6 provided interfaces: recycle(k) do  $\{(msg[k], wasDelivered[k]) \leftarrow ([\emptyset, \emptyset, \emptyset], False)\}$  /* also initialize the communication channel
   with  $p_k$  [6, Ch. 3.1] */
7 macros:
8 msg() begin
9   let  $m[\mathcal{P}][brbMSG] := [[\emptyset, \dots, \emptyset], \dots, [\emptyset, \dots, \emptyset]]$ ;
10   $m[i][init] \leftarrow msg[i][init]$ ;
11  foreach  $p_k \in \mathcal{P} \setminus \{p_i\}$  do  $m[k][init] \leftarrow \emptyset$ ;
12  foreach  $s \in brbMSG \setminus \{init\} \wedge p_k \in \mathcal{P} \wedge (i, m) \in msg[k][s]$  do
13     $m[k][s] \leftarrow m[k][s] \cup \{(i, m)\}$ ;
14  return  $m$ ;
15 mrg( $m_j, j$ ) begin
16   $msg[j][init] \leftarrow m_j[j][init]$ ;
17  foreach  $p_k \in \mathcal{P} \wedge s \in brbMSG \setminus \{init\}$  do
18     $msg[k][s] \leftarrow (msg[k][s] \setminus \{(j, -)\}) \cup m_j[k][s]$ ;
19 operations:
20 brbBroadcast( $v$ ) do if txAvailable() then recycle( $i$ );  $msg[i][init] \leftarrow \{v\}$ ;
21 brbDeliver( $k$ ) begin
22  if  $\exists m : (n-t) \leq |\{p_\ell \in \mathcal{P} : (\ell, m) \in msg[k][ready]\}| \wedge rxAvailable(k)$  then
23     $wasDelivered[k] \leftarrow m \neq \perp$ ; return  $m$ ;
24  else return  $\perp$ ;
25 brbWasDelivered( $k$ ) do return  $wasDelivered[k]$ ;
26 do-forever begin
27   foreach  $p_k \in \mathcal{P}$  do
28     if  $|msg[k][init]| \neq 1 \vee \exists s \neq init : \exists p_\ell \in \mathcal{P} : \exists (\ell, m), (\ell, m') \in msg[k][s] : m \neq m' \vee \exists (\ell, m) \in msg[k][echo] : m \notin msg[k][init] \vee \exists (\ell, m) \in msg[k][ready] : \neg((n+t)/2 < |\{p_{\ell'} \in \mathcal{P} : (\ell', m) \in msg[k][echo]\}| \vee (t+1) \leq |\{p_{\ell'} \in \mathcal{P} : (\ell', m) \in msg[k][ready]\}|) \vee (wasDelivered[k] \wedge brbDeliver(k) = \perp)$  then
29       recycle( $k$ );
30     if  $\exists m \in msg[k][init]$  then  $msg[k][echo] \leftarrow msg[k][echo] \cup \{(i, m)\}$ ;
31     if  $\exists m : (n+t)/2 < |\{p_\ell \in \mathcal{P} : (\ell, m) \in msg[k][echo]\}|$  then
32        $msg[k][ready] \leftarrow msg[k][ready] \cup \{(i, m)\}$ ;
33     if  $\exists m : (t+1) \leq |\{p_\ell \in \mathcal{P} : (\ell, m) \in msg[k][ready]\}|$  then
34        $msg[k][ready] \leftarrow msg[k][ready] \cup \{(i, m)\}$ ;
35   broadcast MSG( $msg()$ ,  $\boxed{txMSG()}$ );
36 upon MSG( $brb_j, \boxed{irc_j}$ ) arrival from  $p_j$  begin
37    $\boxed{rxMSG(brb_j, irc_j, j)}$ ;
38    $mrg(brb_j, j)$ ;

```

Self-stabilizing BFT single-instance solution. Algorithm 3 proposes our SSBFB solution for $BAMP_{n,t}[FC, t < n/3]$. The key ideas are to (i) offer a variance of Algorithm 2, such that its operations always complete even when starting from a corrupted state, (ii) offer interfaces for coordinating the recycling of a given BRB object, as well as (iii) offer interfaces for accessing the delivered value and current status of the broadcast. This way, the recycling coordination mechanism (§ 6) can make sure that no BRB object is recycled before all correct nodes deliver its result. Also, once all correct nodes have delivered a message, the BRB object can eventually be recycled. The boxed code fragments in lines 35 to 36 are irrelevant to our single-instance BRB implementation. Moreover, in line 28 we use the underline font to denote a clause that is only relevant when executing Algorithm 3 as a single instance BRB (and not when running it concurrently with Algorithms 4 and 5). Fig. 1 presents an overview of the solution components.

Types, constants, variables, message structure, and macros. As mentioned, the message MSG() unifies the messages of Algorithm 2. The array $msg[][]$ stores both the information that is sent and arrived at these messages. *i.e.*, for any $j \neq i$, $msg_i[j][\]$ stores the information related to p_j 's BRB object. Also, we define the type $brbMSG := \{init, echo, ready\}$ (line 1) for storing information related to BRB-broadcast messages, for example, $msg_i[i][init]$ stores the information that BRB-broadcast disseminates of INIT() messages and the results of the content of READY() messages appear in $msg_i[i][ready]$. Using the macro $msg()$, p_i can send information that is supported by only p_i , *i.e.*, p_i 's $init$ message as well as p_i 's support

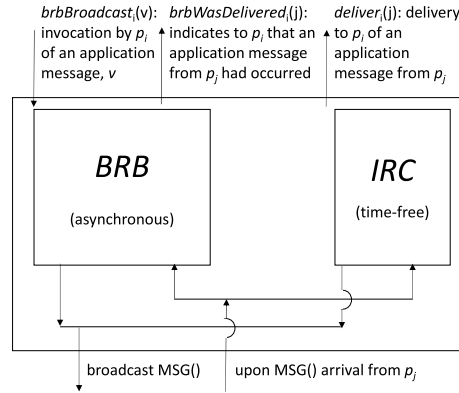


Fig. 1. The solution components.

records (i, m) for phases `echo` and `ready`. The macro $mrg()$ allows the integration of received information with the local one.

The algorithmic transformation. The $brbBroadcast(v)$ operation (line 20) allows Algorithm 3 to invoke BRB-broadcast with message v . The invocation causes Algorithm 3 to follow the logic of the BRB solution in Algorithm 2 in lines 21 to 24 and 30 to 34. Our solution also includes consistency tests at line 28.

Getting delivered values and current status. Algorithms 1 and 2 inform the application about message arrival by raising the events of $ndDeliver()$ (line 5), and respectively, $brbDeliver()$ (line 7). As explained in the context of self-stabilization, a single transient fault can cause the local state to indicate that the message has already been delivered. Our SSBFT BRB solution takes another approach in which the application is pulling information from Algorithm 3 by invoking the $brbDeliver()$ operation (line 21), which returns message m once it has arrived from at least $n - t$ different nodes (line 23). Otherwise, \perp is returned since no message is ready to be delivered (line 24). Observe that once $brbDeliver_i(k) : i, k \in Correct$ returns a non- \perp value, $brbDeliver_i(k)$ returns a non- \perp value in all subsequent invocations.

Satisfying BRB-integrity (Definition 1.1) in a self-stabilizing manner. Line 23 records the fact that the non- \perp message was delivered at least once by storing `True` in $wasDelivered_i[k]$. The application can access the value stored in $wasDelivered_i[k]$ by invoking $brbWasDelivered_i(k)$ (line 25). In other words, whenever the application at p_i tries to pull the arriving message from p_k , it can first test $brbWasDelivered_i(k)$. If `False` is returned, $brbDeliver_i(k)$ can be invoked. Otherwise, the message from p_k was already delivered. Note that the use of $brbWasDelivered()$ is needed only for the single-instance BRB implementation presented by Algorithm 2 since the repeated BRB version uses the recycling mechanism (§ 6) for providing a simpler way to satisfy BRB-integrity.

5. Correctness of Algorithm 3

Definition 5.1 defines the terms *active nodes* and *consistency*. Theorem 5.1 shows that *Convergence* is obtained by consistency regaining (Lemma 5.2). Then, we prove completion from any starting state (Lemma 5.4). The rest of the proof is based on the assumption that BRB objects are eventually recycled after their task was completed (§ 1). The *Closure* proof (§ 5) shows that Algorithm 3 satisfies BRB's requirements (Definition 1.1).

Definition 5.1 (*Active nodes and consistent executions of Algorithm 3*). Node $p_i \in \mathcal{P}$ is said to be *active* when $msg_i[i][init] \neq \emptyset$. Let R be an Algorithm 3's execution, $p_i, p_j \in \mathcal{P} : i \in Correct$, and $c \in R$. Suppose in c : **(brb.i)** The if-statement condition in line 28 does not hold w.r.t. any correct node. **(brb.ii)** any message $MSG(brbJ = mJ, -)$ in transit from p_i to p_j is the result of $msg_i()$. In this case, we say that c is consistent w.r.t. p_i . Suppose every system state in R is consistent w.r.t. p_i . Then we say that R is consistent w.r.t. p_i .

Note the term *active* (Definition 5.1) does not distinguish between the cases in which a node is active due to the occurrence of a transient fault or the invocation of $brbBroadcast(v)$. Our proof uses the term *post-recycled system state* (Definition 5.2), which is also a consistent one (Definition 5.1).

Definition 5.2 (*Post-recycle system states and complete invocation of operations*). We say that system state c is *post-recycled* w.r.t. $p_i \in \mathcal{P} : i \in Correct$ if $\forall j \in Correct : (msg_j[i], wasDelivered_j[i]) = (\emptyset, \emptyset, \emptyset), False$ holds and no communication channel from p_i to p_j includes $MSG(brbJ \neq [\emptyset, \dots, \emptyset], -)$. Suppose that execution R starts in the post-recycled system state c and p_i invokes $brbBroadcast_i(v)$ exactly once. In this case, we say that R includes a *complete BRB invocation* w.r.t. p_i .

Algorithm 3's Convergence. Theorem 5.1 bounds the stabilization time of Algorithm 3 using asynchronous cycles (§ 2) while considering fair executions in which all correct nodes are assumed to complete roundtrips infinitely often with all other correct nodes.

The Convergence proof (Theorem 5.1) shows that, within $\mathcal{O}(1)$ asynchronous cycles, *w.r.t.* any correct p_i , the state of p_i 's BRB object reaches its post-recycling state (Definition 5.2). Our recycling mechanism facilitates the satisfaction of this requirement. That is, within $\mathcal{O}(1)$ asynchronous cycles, p_i 's BRB object has either reached a post-recycling state or has completed its task, and thus it is ready to be recycled. Specifically, Lemma 5.2 demonstrates consistency within $\mathcal{O}(1)$ asynchronous cycles. Lemma 5.3 shows that if a correct node is not active, its BRB object reaches a post-recycling state within $\mathcal{O}(1)$ asynchronous cycles. Lemma 5.6 shows that if a correct node is active, the object completes its task within $\mathcal{O}(1)$ asynchronous cycles, and thus, it can be recycled.

Theorem 5.1 (Algorithm 3's Convergence). *Let R be a fair execution of Algorithm 3. Within $\mathcal{O}(1)$ asynchronous cycles, Convergence is done.*

Proof of Theorem 5.1.

Lemma 5.2 (Consistency). *Let $i \in \text{Correct}$. Within $\mathcal{O}(1)$ asynchronous cycles, the system reaches a state $c \in R$ that starts a consistent execution *w.r.t.* p_i (Definition 5.1).*

Proof of Lemma 5.2. Suppose that R 's starting state is not consistent *w.r.t.* p_i . Specifically, suppose that Invariant (brb.i) does not hold. *I.e.*, the if-statement condition in line 28 holds. Since R is fair, within $\mathcal{O}(1)$ asynchronous cycles, p_i takes a step that includes the execution of line 29, which assures that R becomes consistent *w.r.t.* p_i (brb.i). By Algorithm 3's code, once Invariant (brb.i) holds *w.r.t.* p_i in $c \in R$, it holds in state $c' \in R$ that follows c .

Let m be a message that in R 's starting state resides in a channel from p_i . Since the channel preserves the FIFO order, if m is ever delivered during R , then it does so within $\mathcal{O}(1)$ asynchronous cycles. By line 35, within $\mathcal{O}(1)$ asynchronous cycles, (brb.ii) holds since it is sufficient to consider only messages that were sent during R from nodes in which (brb.i) holds. $\square_{\text{Lemma 5.2}}$

Lemma 5.3 (Completion starting from any system state and no active broadcaster). *Suppose $p_i \in \mathcal{P} : i \in \text{Correct}$ is not active. Within $\mathcal{O}(1)$ asynchronous cycles, $\forall j \in \text{Correct} : (\text{msg}_j[i], \text{wasDelivered}_j[i]) = (\{\emptyset, \emptyset, \emptyset\}, \text{False})$.*

Proof of Lemma 5.3. The proof follows from the assumption that $\text{msg}_i[i][\text{init}] = \emptyset$ throughout R , Argument (1), and Lemma 5.2.

Argument (1) *Within $\mathcal{O}(1)$ asynchronous cycles, $\text{msg}_i[i][\text{init}] = \text{msg}_j[i][\text{init}]$ holds if $\text{msg}_i[i][\text{init}]$'s values do not change during R . Since p_i is correct, it broadcasts $\text{MSG}(\text{brb}J = \text{msg}_i, -)$ within $\mathcal{O}(1)$ asynchronous cycles. Since R is fair, every correct $p_j \in \mathcal{P}$ receives $\text{MSG}(\text{brb}J, -)$ within $\mathcal{O}(1)$ asynchronous cycles. Thus, the argument holds (due to line 16). $\square_{\text{Lemma 5.3}}$*

Lemma 5.4 (Completion starting from any system state and an active broadcaster). *Suppose $p_i \in \mathcal{P} : i \in \text{Correct}$ is active throughout R . Within $\mathcal{O}(1)$ asynchronous cycles, $\forall i, j \in \text{Correct} : \text{brbDeliver}_j(i) \neq \perp$.*

Proof of Lemma 5.4. Since p_i is active, $\text{msg}_i[i][\text{init}] = \{m\}$. By Lemma 5.3's Argument (1), $\text{msg}_j[i][\text{init}] = \{m\}$ within $\mathcal{O}(1)$ asynchronous cycles. Also, $\forall j \in \text{Correct} : \text{msg}_j[j][\text{echo}] \supseteq \{(i, m)\}$ since node p_j observes that the if-statement condition in line 30 holds (for the case of $k_j = i$). Then, p_j broadcasts $\text{MSG}(\text{brb}J = \text{msg}_j, -)$ at least once in every $\mathcal{O}(1)$ asynchronous cycles. Since R is fair, every correct $p_\ell \in \mathcal{P}$ receives $\text{MSG}(\text{brb}J, -)$ within $\mathcal{O}(1)$ asynchronous cycles. Thus, $\forall j, \ell \in \text{Correct} : \text{msg}_\ell[j][\text{echo}] \supseteq \{(i, m)\}$ (line 17). Since $n-t > (n+t)/2$, p_ℓ observes that $(n+t)/2 < |\{p_x \in \mathcal{P} : (x, m) \in \text{msg}_\ell[j][\text{echo}]\}|$ holds, *i.e.*, the if-statement condition in line 31 holds, and thus, $\text{msg}_\ell[\ell][\text{ready}] \supseteq \{(i, m)\}$ holds. Note $t < (n+t)/2$, faulty nodes cannot prevent a correct node from broadcasting $\text{MSG}(\text{brb}J = mJ, -) : mJ[i][\text{ready}] \supseteq \{(i, m)\}$ at least once every $\mathcal{O}(1)$ asynchronous cycles, say, by colluding and sending $\text{MSG}(\text{brb}J = mJ, -) : mJ[i][\text{ready}] \supseteq \{(i, m')\} \wedge m' \neq m$. Since R is fair, any correct $p_y \in \mathcal{P}$ receives $\text{MSG}(\text{brb}J = mJ, -)$ within $\mathcal{O}(1)$ asynchronous cycles. Thus, $\forall j, y \in \text{Correct} : \text{msg}_y[j][\text{ready}] \supseteq \{(i, m)\}$ holds (line 17). When p_y invokes $\text{brbDeliver}_y(i)$ (line 21), $\exists_m(n-t) \leq |\{p_\ell \in \mathcal{P} : (\ell, m) \in \text{msg}_y[k_y = i][\text{ready}]\}|$ holds and m is returned. $\square_{\text{Lemma 5.4}} \square_{\text{Theorem 5.1}}$

Closure of BRB-broadcast. The Closure proof considers post-recycled system states and complete invocation of operations (Definition 5.2).

Theorem 5.5 (BRB Closure). *Let R be an Algorithm 3's execution that starts in a post-recycled and in which every correct node eventually invokes a complete BRB-broadcast. R demonstrates a BRB construction.*

Proof of Theorem 5.5. Lemma 5.6 shows operation completion within a finite time. Lemma 5.6's proof is by similar arguments to the ones of Lemma 5.4. The main difference is that fairness assumptions are not considered (§ 2), and thus, we demonstrate that BRB-Completion-1 holds eventually.

Lemma 5.6 (BRB-Completion-1). *BRB-Completion-1 holds.*

Proof of Lemma 5.6. Since p_i is correct, it broadcasts $\text{MSG}(\text{brbJ} = \text{msg}_i, -)$ infinitely often, where $\text{msg}_i[i][\text{init}] = \{m\}$. By fair communication, every correct p_j receives $\text{MSG}(\text{brbJ}, -)$ eventually. Thus, $\forall j \in \text{Correct} : \text{msg}_j[i][\text{init}] = \{m\}$ (line 17). Also, $\forall j \in \text{Correct} : \text{msg}_j[j][\text{echo}] \supseteq \{(i, m)\}$ since p_j 's if-statement in line 30 holds (for the case of $k_j = i$). Then, p_j broadcasts $\text{MSG}(\text{brbJ} = \text{msg}_j, -)$ infinitely often. By fair communication, every correct p_ℓ receives $\text{MSG}(\text{brbJ}, -)$ eventually. Thus, $\forall j, \ell \in \text{Correct} : \text{msg}_\ell[j][\text{echo}] \supseteq \{(i, m)\}$ (line 17). Since $n-t > (n+t)/2$, p_ℓ observes that $(n+t)/2 < |\{p_x \in \mathcal{P} : (x, m) \in \text{msg}_\ell[i][\text{echo}]\}|$ holds, i.e., the if-statement in line 31 holds, and thus, $\text{msg}_\ell[i][\text{ready}] \supseteq \{(i, m)\}$ holds. Note that, since $t < (n+t)/2$, faulty nodes cannot prevent a correct node from broadcasting $\text{MSG}(\text{brbJ} = mJ, -) : mJ[i][\text{ready}] \supseteq \{(i, m)\}$ infinitely often, say, by colluding and sending $\text{MSG}(\text{brbJ} = mJ, -) : mJ[i][\text{ready}] \supseteq \{(i, m')\} \wedge m' \neq m$. By fair communication, every correct $p_y \in \mathcal{P}$ receives $\text{MSG}(\text{brbJ} = mJ, -)$ eventually. Thus, $\forall j, y \in \text{Correct} : \text{msg}_y[j][\text{ready}] \supseteq \{(i, m)\}$ holds (line 17). Therefore, whenever p_y invokes $\text{brbDeliver}_y(i)$ (line 21), the condition $\exists_m(n-t) \leq |\{p_\ell \in \mathcal{P} : (\ell, m) \in \text{msg}_y[i][\text{ready}]\}|$ holds, and thus, m is returned. $\square_{\text{Lemma 5.6}}$

Lemma 5.7 (BRB-Completion-2). *BRB-Completion-2 holds.*

Proof of Lemma 5.7. By line 22, p_i can BRB-deliver m from p_j only once $\exists_m(n-t) \leq |\{p_\ell \in \mathcal{P} : (\ell, m) \in \text{msg}_j[j][\text{ready}]\}|$ holds. Due to the assumption that R 's starting state is post-recycled, only lines 18 and 32 to 34 can add items to $\text{msg}_j[j][\text{ready}]$. Let $\text{MSG}(mJ, -)$ be a message such that $(\ell, m) \in mJ[j][\text{ready}]$. Specifically, line 18 adds to $\text{msg}_j[j][\text{ready}]$ items according to information in $\text{MSG}(mJ, -)$ messages coming from, say, p_ℓ . I.e., infinitely often, at least $t+1$ distinct and correct nodes, p_ℓ , broadcast $\text{MSG}(mJ, -) : (\ell, m) \in mJ[j][\text{ready}]$. By fair communication and line 18, all correct nodes, p_x , eventually receive $\text{MSG}(mJ, -) : (\ell, m) \in mJ[j][\text{ready}]$ from at least $t+1$ distinct nodes, p_ℓ , and make sure that $(\ell, m) \in \text{msg}_x[j][\text{ready}]$. By line 34, $\text{msg}_x[j][\text{ready}] \supseteq \{(x, m)\}$, i.e., every correct node broadcasts $\text{MSG}(mJ, -) : (x, m) \in mJ[j][\text{ready}]$ infinitely often. By fair communication and line 18, all correct p_y receive $\text{MSG}(mJ, -) : (x, m) \in mJ[j][\text{ready}]$ from at least $n-t$ distinct nodes, p_x , eventually. I.e., $\exists_m(n-t) \leq |\{p_x \in \mathcal{P} : (x, m) \in \text{msg}_y[j][\text{ready}]\}|$ holds (due to line 18) and $\forall i \in \text{Correct} : \text{brbDeliver}_i(j) \neq \perp$. $\square_{\text{Lemma 5.7}}$

Lemma 5.8. *The BRB-integrity property holds.*

Proof of Lemma 5.8. Suppose $\text{brbDeliver}_i(k) = m \neq \perp$ holds in $c \in R$, where $i \in \text{Correct} \wedge p_k \in \mathcal{P}$. Also, (towards a contradiction) $\text{brbDeliver}_i(k) = m' \notin \{\perp, m\}$ holds in $c' \in R$, where c' appears after c in R . In other words, $\exists_m(n-t) \leq |\{p_\ell \in \mathcal{P} : (\ell, m) \in \text{msg}_i[k][\text{ready}]\}|$ in c and $\exists_{m'}(n-t) \leq |\{p_\ell \in \mathcal{P} : (\ell, m') \in \text{msg}_i[k][\text{ready}]\}|$ in c' . Since $n \geq 3t+1$ and R starts in a post-recycling state (and thus consistent), there is $p_\ell \in \mathcal{P}$, such that $(\ell, m) \in \text{msg}_i[k][\text{ready}]$ in c and $(\ell, m') \in \text{msg}_i[k][\text{ready}]$ in c' . Algorithm 3 does not remove elements from any entry $\text{msg}_i[k][\text{ready}]$ since R is consistent. This means that $\text{msg}_i[k][\text{ready}]$ includes both (ℓ, m) and (ℓ, m') in c' . However, this contradicts the fact that c' is consistent (Lemma 5.2). Thus, $\nexists c' \in R$ and BRB-integrity holds. $\square_{\text{Lemma 5.8}}$

Lemma 5.9 (BRB-validity). *BRB-validity holds.*

Proof of Lemma 5.9. Let $i, j \in \text{Correct}$. Suppose p_j BRB-delivers m from p_i . The proof shows that p_i BRB-broadcasts m . I.e., suppose the adversary, who captures up to t (Byzantine) nodes, sends "fake" messages of $(i, m) \in \text{msg}_j[i][\text{echo}]$ or $\text{msg}_j[i][\text{ready}]$, but p_i , who is correct, never invoked $\text{brbBroadcast}(m)$. In this case, our proof shows that no correct node BRB-delivers (i, m) . This is because there are at most t nodes that can broadcast "fake" messages. Thus, $\text{brbDeliver}(k)$ (line 21) cannot deliver (i, m) since $t < n-t$, i.e., $\nexists_m(n-t) \leq |\{p_\ell \in \mathcal{P} : (\ell, m) \in \text{msg}_i[i][\text{ready}]\}|$. $\square_{\text{Lemma 5.9}}$

Lemma 5.10 (BRB-no-duplcity). *BRB-no-duplcity holds.*

Proof of Lemma 5.10. Let $i, j \in \text{Correct}$ and $p_k \in \mathcal{P}$. Suppose that p_i and p_j broadcast $\text{MSG}(mJ, -) : mJ[k][\text{ready}] = \{(i, m)\}$, and respect., $\text{MSG}(mJ, -) : mJ[k][\text{ready}] = \{(j, m')\}$. We prove BRB-no-duplcity by showing that $m = m'$. Since R is post-recycling, there must be a step in R in which the element $(x, -)$ is added to $\text{msg}_x[k][\text{ready}]$ for the first time during R , where $p_x \in \{p_i, p_j\}$. The correctness proof considers the following two cases.

• **Both p_i and p_j add $(x, -)$ due to line 32.** Suppose, towards a contradiction, that $m \neq m'$. Since the if-statement condition in line 32 holds for both p_i and p_j , we know that $\exists_m(n+t)/2 < |\{p_\ell \in \mathcal{P} : (\ell, m) \in \text{msg}_i[k][\text{echo}]\}|$ and $\exists_{m'}(n+t)/2 < |\{p_\ell \in \mathcal{P} : (\ell, m') \in \text{msg}_j[k][\text{echo}]\}|$ hold. Since R is post-recycling, this can only happen if p_i and p_j received $\text{MSG}(mJ, -) : mJ[k][\text{echo}] \supseteq \{(\ell, m)\}$, and respect., $\text{MSG}(mJ, -) : mJ[k][\text{echo}] \supseteq \{(\ell, m')\}$ from $(n+t)/2$ distinct nodes, p_ℓ . Note that $\exists p_x \in$

$Q_1 \cap Q_2 : x \in \text{Correct}$, where $Q_1, Q_2 \subseteq \mathcal{P} : |Q_1|, |Q_2| \geq 1 + (n+t)/2$ (as in Raynal [1], item (c) of Lemma 3). But, any correct node, p_x , sends at most one element in $\text{msg}_x[k][\text{echo}]$ (lines 8 to 14 and 35) during R . Thus, $m = m'$, which contradicts the case assumption.

• **There is $p_x \in \{p_i, p_j\}$ that adds $(x, -)$ due to line 34.** I.e., $\exists m''(t+1) \leq |\{p_\ell \in \mathcal{P} : (\ell, m'') \in \text{msg}_x[k][\text{ready}]\}| \wedge m'' \in \{m, m'\}$. Since there are at most t faulty nodes, p_x received $\text{MSG}(m) : \text{mj}[k][\text{ready}] = \{(\ell, m'')\}$ from at least one correct node, say p_{x_1} , which received $\text{MSG}(m) : \text{mj}[k][\text{ready}] = \{(k, m'')\}$ from p_{x_2} , and so on. This chain cannot be longer than n and it must be originated by the previous case in which $(x, -)$ is added due to line 32. Thus, $m = m'$. $\square_{\text{Lemma 5.10}} \square_{\text{Theorem 5.5}}$

6. Self-stabilizing BRB object recycling in message-passing systems

We present the *Independent Round Counter* (IRC) task, which can implement $\text{txAvailable}()$ and $\text{rxAvailable}(k)$ (Fig. 4 and Algorithm 3) for the model of $\text{BAMP}_{n,t}[\text{FC}, t < n/3, \diamond P_{\text{mute}}, \text{BML}]$. This section also explains how to enrich the model presented in § 2 with muteness detectors and the BML assumption.

In non-self-stabilizing node-failure-free systems, the operations $\text{txAvailable}()$ and $\text{rxAvailable}(k)$ can be implemented using prevailing mechanisms for ARQ, which use unbounded counters. These mechanisms are often used for guaranteeing reliable communications by letting the sender collect acknowledgments from all receivers. Each message is associated with a unique message number, which the sender obtains by adding one to the previous message number after all acknowledgments arrived. From that point in time, the previous message number is obsolete and can be recycled.

For the case of self-stabilizing node-failure-free systems, the challenge is to deal with integer overflow events. Specifically, when an algorithm considers the counters to be unbounded but the studied system has bounded memory, transient faults can trigger integer overflow events. The solution presented here shows how to overcome this challenge via a mild synchrony assumption and muteness detection.

Independent Round Counters (IRCs). The IRC task (Definition 6.1) considers n independent counters. Each counter, cnt_i , can be incremented only by a unique node, $p_i \in \mathcal{P}$, via the invocation of the $\text{increment}_i()$ operation, which returns the new round number or \perp when the invocation is (temporarily) disabled. Suppose $p_i, p_j \in \mathcal{P}$ are correct. Every node $p_j \in \mathcal{P}$ can fetch cnt_i 's value via the invocation of the $\text{fetch}_j(i)$ operation, which returns the most recent and non-fetched cnt_i 's value or \perp when such value is currently unavailable. Algorithms that satisfy Definition 6.1 provide an implementation of $\text{txAvailable}()$ and $\text{rxAvailable}(k)$ by returning $\text{increment}() \neq \perp$ and $\text{fetch}(k) \neq \perp$, respectively.

Definition 6.1 (*Independent Round Counters (IRCs)*). We define the IRC task using the following requirements.

- **IRC-validity.** Suppose p_j IRC-fetches s from cnt_i . Then, p_i had IRC-incremented cnt_i to s .
- **IRC-integrity-1.** No correct node IRC-fetches a value more than once from the counter of any other correct node (considering the B most recent IRC-fetches). In detail, let $S_{i,j} = (s_0, \dots, s_x) : x < B$ be a sequence of p_i 's round numbers that p_j fetched—we are only interested in B most recent ones, where B is a predefined constant. It holds that $\forall s_y \in S_{i,j} : y < B - 1 \implies s_y + 1 \bmod (B + 1) = s_{y+1}$. In other words, $S_{i,j}$ is a sequence that includes all the integers that s takes when incrementing s modulo $B + 1$, starting from s_0 and ending at s_x .
- **IRC-integrity-2.** Correct nodes that IRC-fetch numbers from cnt_i do so in the order in which cnt_i was incremented (considering the B most recent IRC-fetches).
- **IRC-preemption.** Suppose p_i IRC-increments cnt_i to s . IRC-increment is (temporarily) disabled until all correct nodes have fetched s from p_i 's counter.
- **IRC-completion.** Suppose all correct nodes IRC-fetch p_i 's counter infinitely often. Node p_i 's IRC-increment is enabled infinitely often.

Bounded message lifetime (BML). Consider a scenario in which, due to a transient fault, p_i 's copy of its round counter is smaller than p_j 's copy of p_i 's counter, say, by $x \in \mathbb{Z}^+$, thus node p_i will have to complete x rounds before p_j could IRC-fetch a non- \perp value. $\text{BAMP}_{n,t}[\text{FC}, t < n/3, \diamond P_{\text{mute}}, \text{BML}]$ overcomes this challenge by following Assumption 6.1.

Assumption 6.1 (*Bounded message lifetime, BML*). Let R be an execution in which a correct $p_i \in \mathcal{P}$ repeatedly broadcasts the protocol messages and completes an unbounded number of roundtrips with every correct, $p_j \in \mathcal{P}$. Suppose p_j receives message $m(s)$ from p_i immediately before $c \in R$, where $s \in \mathbb{Z}^+$ is the round number. We assume $(\text{rnd}_i - s) \bmod (B + 1) \leq \lambda$ in c , where rnd_i is p_i 's round number, $\lambda \in \mathbb{Z}^+ : \text{channelCapacity} < \lambda < B/6$ is a known upper-bound, channelCapacity is defined in § 2, and B is a predefined bound on integer size.

We would like to clarify that Assumption 6.1 accounts for the scenario where the value of s can differ from rnd_i during Convergence. As mentioned in Section 2, the latter refers to the recovery period that follows the last transient fault occurrence.

Muteness Detection. $\text{BAMP}_{n,t}[\text{FC}, t < n/3, \diamond P_{\text{mute}}, \text{BML}]$ also provides an SSBFT detector for muteness failures of class $\diamond P_{\text{mute}}$.

Algorithm 4: SSBFT IRC; code for p_i .

```

39 constants:  $B$ : a predefined bound on the integer size, say,  $2^{64} - 1$ ;
40 variables:  $cur[\mathcal{P}] = [-1, \dots, -1]$  and  $nxt[\mathcal{P}] = [-1, \dots, -1]$ : a pair of round number arrays. Each entry in each array is associated with a single
    system node. The entry  $cur[i]$  is  $p_i$ 's current round number. Also,  $cur[j]$  stores the most recently received round number from  $p_j$ . The array  $nxt[]$ 
    stores on the  $j$ -th entry the next sequence number to be recycled (once it is delivered to all);
41  $txLb[\mathcal{P}] = [0, \dots, 0]$ : sender-side round-trip labels that are used in the context of round number  $cur[i]$ , where  $txLb[j]$  stores the most recently
    received label from the receiver  $p_j$ . [Also, the assignment of a new round number to  $cur[i]$  leads to  $txLb[j]$ 's nullification, cf. line 73.];
42  $rxLb[\mathcal{P}] = [0, \dots, 0]$ : receiver-side round-trip labels, where  $rxLb[j]$  stores the most recently received label from the sender  $p_j$ ;
43 required interfaces:  $recycle(k)$ ,  $trusted()$ ,  $invoc()$ ,  $rtComp(j)$ ;
44 provided interface:
45  $txAvailable()$  do {return  $increment() \neq \perp$ };
46  $rxAvailable(k)$  do {return  $fetch(k) \neq \perp$ };
47 macro:  $behind(d, s, c)$  do {return  $s \in \{x \bmod (B + 1) : x \in \{c - d\lambda, \dots, c\}\}$ };
48 operation  $increment()$  begin
49   if  $cur[i] \neq -1 \wedge \exists j \in trusted(): txLb[j] \leq 2(channelCapacity + 1)$  then return  $\perp$ ;
50   else
51      $invoc()$ ;
52      $(cur[i], txLb[i]) \leftarrow ((cur[i] + 1) \bmod (B + 1), [0, \dots, 0])$ ;
53      $recycle(i)$ ;
54     return  $cur[i]$ ;
55 operation  $fetch(k)$  begin
56   if  $behind(1, cur[k], nxt[k])$  then return  $\perp$ ;
57   else  $\{nxt[k] \leftarrow cur[k];$  return  $nxt[k]\}$ ;
58 operation  $txMSG(j)$  {return  $(cur[i], nxt[j], txLb[j], rxLb[j])$ };
59 operation  $rxMSG(brbJ, ircJ = (curJ, nxtJ, txJ, rxJ), j)$  begin
60   if  $behind(2, cur[i], nxtJ) \wedge txLb[j] = rxJ$  then
61      $rtComp(j)$ ;
62      $txLb[j] \leftarrow \min\{B, txLb[j] + 1\}$ ;
63   else
64     if  $\neg behind(1, curJ, cur[j])$  then  $\{cur[j] \leftarrow curJ; recycle(j)\}$ ;
65      $rxLb[j] \leftarrow txJ$ ;
66 do forever foreach  $p_j \in \mathcal{P}$  send  $MSG(msg(), txMSG(j))$ ;
67 upon  $MSG(brbJ, ircJ)$  arrival from  $p_j$  begin
68    $rxMSG(brbJ, ircJ, j)$ ;
69    $mrg(brbJ, j)$ ;

```

Muteness Failures. Let us consider an algorithm, Alg , that attaches a round number, $seq \in \mathbb{Z}^+$, to every message, $m(seq)$ that it sends. Suppose there is a system state $c_\tau \in R$ after which p_j stops forever replying to p_i 's messages, $m(seq)$, where $p_i, p_j \in \mathcal{P}$. In this case, we say that p_j is *mute* to p_i w.r.t. message $m(seq)$. We clarify that a Byzantine node is not mute if it forever sends all the messages required by Alg . For the sake of a simple presentation, we assume that the syntax of $m(seq)$ corresponds to the syntax of a message generated by Alg (since, otherwise, the receiver may simply omit messages with syntax errors). Naturally, the data load of those messages can be wrong. Observe that the set of mute nodes also includes all crashed nodes.

Muteness Detection. The class $\diamond P_{mute}$ of muteness detectors (Definition 6.2) assumes the availability of the interface function, $trusted()$.

Definition 6.2 (Specifications of eventual muteness failure detector). The function $trusted()$ returns the set of unsuspected node identifiers, such that:

- **Strong Completeness:** Eventually, every mute node is forever suspected w.r.t. round number s by every correct node (or the round number changes).
- **Eventual Strong Accuracy:** Eventually, the system reaches a state $c_\tau \in R$ in which no correct node is suspected.

SSBFT IRC for $BAMP_{n,t}[FC, t < n/3, \diamond P_{mute}, BML]$. Algorithm 4 makes sure that any node that had IRC-incremented its round counter defers any further IRC-increments until all nodes have acknowledged the latest IRC-increment. Algorithm 4's line numbers continue the ones of Algorithm 3. Also, we defer the presentation of the `boxed` code in lines 51 and 61 until we present the implementation of our muteness detector (Algorithm 5). We clarify that when these `boxed` code lines are

excluded, Algorithm 4 loses its ability to tolerate node failures, even when considering only crashes. Thus, it is imperative for the correctness proof (§ 7) to consider both the boxed code lines and Algorithm 5 when demonstrating the properties of Algorithm 4. In other words, the division between Algorithms 4 and 5 serves the sole purpose of presentation simplicity.

Constants and variables. All integers used by Algorithm 4 have a maximum value, which we denote by B (line 39), that is required to be large, say, $2^{64} - 1$. The arrays $cur[]$ and $nxt[]$ (line 40) store round numbers. The entry $cur[i]$ is p_i 's current round number and $cur[j]$ stores the most recently received round number from each other node p_j . The array $nxt[]$ stores on the j -th entry the next sequence number to be recycled (once it is delivered to all). The array $txLbl[]$ holds labels that are used for counting the number of round-trips that p_i is able to complete in the context of the current round number, $cur[i]$, where $txLbl[j]$ is the most recently received label from p_j (line 40). The array $rxLbl[\mathcal{P}] = [0, \dots, 0]$ holds the receiver-side round-trip labels, where $rxLbl[j]$ stores the most recently received label from the sender p_j . These values allow the receiver to acknowledge the arriving labels back to the sender.

Interfaces. We remind the implementation of interface function $recycle()$ (line 43) is provided by Algorithm 3, line 6. Algorithm 4 offers implementations for the interfaces $txAvailable()$ (line 45) and $rxAvailable(k)$ (line 46).

Macros. The macro $behind(d, s, c)$ (line 47) takes three input parameters, *i.e.*, $d \in \{1, 2\}$ and two sequence numbers, s and c . It allows Algorithm 4 to test whether s is a member of the set of numbers that are between $c - d\lambda$ and c while taking integer overflow considerations using modulo $B + 1$ operations, where λ is a bound on the message lifetime (BML), see Assumption 6.1. Note that the parameter $d \in \{1, 2\}$ allows $behind(d, s, c)$ to test whether c is not 'too old' when simple transmissions ($d = 1$) or roundtrip exchanges ($d = 2$) are considered.

The increment() operation. This operation allows the caller to IRC-increment the value of its round number modulo $B + 1$ and nullify all labels in $txLbl[]$ (line 52) as well as recycle the relevant BRB object (line 53) and return the new round number (line 54). However, if the previous invocation has not finished, the operation is disabled and the \perp value is returned. Line 49 tests whether the round number is ready to be incremented. In detail, line 49 checks whether this is the first round, *i.e.*, a round number of -1 , or the previous round has finished, *i.e.*, the labels indicate that every node has completed at least $2(\text{channelCapacity} + 1)$ roundtrips. By exchanging at least $2(\text{channelCapacity} + 1)$ labels, the proposed solution overcomes packet loss and duplication over FIFO channels, see [8] for a more efficient variation on this technique that also deals with packet reordering.

The fetch(k) operation. This operation returns, exactly once, the most recently received round number. Line 56 tests whether a new round number has arrived. If this is not the case, then \perp is returned. Otherwise, the value of the new round number is returned (line 57). In detail, due to Assumption 6.1, immediately after the arrival of message $m(s)$ to p_j from p_i , the fact that $s \notin \{x \bmod (B + 1) : x \in [c - 1\lambda, \dots, c]\}$ holds implies that s is newer than $cur_j[i]$. Thus, p_i can use $behind_i(1, cur_j[k], nxt_i[k])$ (line 56) for testing the freshness of the round number stored in $cur_i[k]$ *w.r.t.* $nxt_i[k]$. In case the number is indeed fresh, $fetch_i()$ updates $nxt_i[k]$ with the returned round number.

The txMSG() and rxMSG() operations. The operations $txMSG()$ and $rxMSG()$ let the sender, and respectively the receiver, process messages. Algorithm 4 sends via the message $MSG()$ two fields: $brbJ$ and $ircJ$, where the field $brbJ$ corresponds to the one of Algorithm 3. Recall that when a message arrives from p_j , the receiving-side adds the suffix J to the field name, *i.e.*, $brbJ$ and $ircJ$. The field $ircJ$ is composed of the fields ack , which indicates whether a reply is required, seq , which is the sender's round number, and lbl , which, during legal executions, is the corresponding label to seq that the sender uses for the receiver. The operation $txMSG()$ (line 58) is used when the sender transmits a message (line 66). It includes the sender's current round number, *i.e.*, $cur[i]$, and the corresponding label that the sender uses for the receiver $p_j \in \mathcal{P}$, *i.e.*, $txLbl[j]$. It also includes the values that are needed for letting the receiver to send an acknowledgment back to the sender, p_j , *i.e.*, $nxt[j]$, and the corresponding label, *i.e.*, $rxLbl[j]$.

The operation $rxMSG()$ processes messages arriving to the sender and the receiver. On the sender-side (lines 60 to 62), when an acknowledgment arrives from the receiver, p_j , the sender checks whether the arriving message has a fresh round number and label (line 60). In this case, the label is incremented (line 62) in order to indicate that at least one roundtrip was completed. In detail, p_i uses $behind_i(2, cur_j[j], sj)$ for testing whether the arriving round number, sj , is fresh by asking whether sj is not a member of the set $\{cur_j[i] - 2\lambda, \dots, cur_j[i]\}$, see Assumption 6.1. As we will see in the next paragraph, there is a need to take into account the receiver's test (line 64), which can cause a non-fresh value to be a member of the set $\{x \bmod (B + 1) : x \in [c - 2\lambda, \dots, c]\}$, but not the set $\{x \bmod (B + 1) : x \in [c - \lambda, \dots, c]\}$.

On the receiver-side (lines 64 and 65), p_i uses $behind_i(1, sj, cur_i[j])$ to test whether a new round number arrived, *i.e.*, testing whether the arriving number, sj , is a member of $\{cur_j[i] - \lambda, \dots, cur_j[i]\}$. In this case, the local round number is updated (line 64) and the interface function $recycle_i(j)$ is called. Note that whenever the receiver gets a message, it stores the arriving sender's label (line 65) so that it could be acknowledged (line 66). That acknowledgment includes the most recently delivered round number, *i.e.*, $nxt[i]$, and the arriving label, l_j 's value, which is stored at $rxLbl_i[j]$.

The 'do forever loop' and message arrival. Note that the processing of messages (for sending and receiving) is along the lines of Algorithm 3. The 'do forever loop' broadcasts the message $MSG()$ to every node in the system (line 66). The operation $txMSG()$ is used for setting the value of the $ircJ$ field. Upon message arrival, as in Algorithm 3, the receiver merges the arriving information (line 67) and passes the arriving values to $rxMSG()$ for processing.

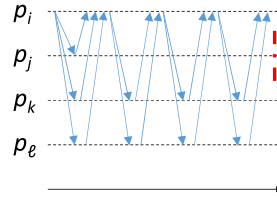


Fig. 2. Blanchard et al.’s implementation with $\Theta = 6$ and no Byzantine failures. In this scenario, node p_j crashes after a single communication round. The dashed line marks the point in time in which p_i suspects p_j to be faulty once nodes p_k and p_l complete three additional rounds. (For interpretation of the colors in the figure(s), the reader is referred to the web version of this article.)

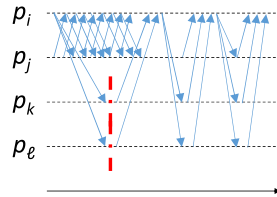


Fig. 3. Blanchard et al.’s implementation with $\Theta = 6$ and a single Byzantine node, p_j . In this scenario, p_j sends six fake replies to messages before their arrival. This leads p_i to falsely suspect the correct nodes p_k and p_l , see the dashed red line.

Our SSBFT implementation of $\diamond P_{mute}$. As mentioned, Algorithm 4 is our SSBFT recycling mechanism for $BAMP_{n,t}[FC, t < n/3, \diamond P_{mute}, BML]$, which requires the implementation of $\diamond P_{mute}$ (Definition 6.2). We present our $\diamond P_{mute}$ ’s implementation (Algorithm 5) and its interface with Algorithm 4, see the boxed code lines. Algorithm 5’s line numbers continue the ones of Algorithm 4. Specifically, Algorithm 4 lets p_i restart the local state of the muteness detector via a call to $invoc_i()$ (line 51). It uses $rtComp_i(j)$ (line 61) for taking into account the completion of a roundtrip between p_i and p_j .

Related solutions. In the context of self-stabilizing Byzantine-free (crash-prone) systems, Blanchard et al. [37] designed a solution for crash-tolerant systems, rather than BFT one. They implemented perfect failure detectors, specifically class P , by allowing node p_i to suspect any node $p_j \in \mathcal{P}$ when p_i is able to complete Θ roundtrips with other nodes in \mathcal{P} but not with p_j , where Θ is a predefined constant. Fig. 2 illustrates the fact that any node p_j that has crashed is unable to respond to messages from p_i , while any correct node can successfully complete roundtrips with p_i . As p_i completes more and more roundtrips with the correct nodes, it eventually suspects node p_j since no roundtrips are completed with p_j . Blanchard et al.’s solution protects the system against adversarial attacks that cause all message deliveries to be deferred in the network, as it is free from assumptions regarding the time it takes to complete a round trip. Blanchard et al.’s time-free solution simply requires the knowledge of an appropriate value for Θ .

Since the studied fault model includes Byzantine failures, we cannot directly borrow such earlier proposals, such as the one by Blanchard et al. Consider, for example, a Byzantine node that anticipates the sender’s messages and transmits acknowledgments before the arrival of prospective messages. Using this attack of speculative acknowledgments, the adversity may accelerate the (false) completion of roundtrips and let the unreliable failure-detector suspect non-faulty nodes. Fig. 3 illustrates such attacks on Blanchard et al., which was designed to be crash-tolerant rather than BFT.

Our solution in a nutshell. As we explain next, our solution relies on Assumption 6.2, which facilitates the defense against the above attacks that use speculative acknowledgments. Specifically, when testing whether the Θ threshold has been exceeded, p_i ignores the roundtrips that were completed with the top t nodes, say *w.l.o.g.*, p_1, \dots, p_t , that had the highest number of roundtrips with p_i . Suppose, *w.l.o.g.*, that nodes $p_{n-t}^{byz}, \dots, p_{n-1}^{byz}$ are captured by the adversary. On the one hand, the adversary aims at letting $p_{n-t}^{byz}, \dots, p_{n-1}^{byz}$ to rapidly complete roundtrips with p_i (since it tries to fool the muteness detector and cause it to suspect correct nodes). While on the other hand, if any of the nodes $p_{n-t}^{byz}, \dots, p_{n-1}^{byz}$ complete roundtrips with p_i faster than any of the nodes p_1, \dots, p_t , the former, p_k^{byz} , is ignored by p_i when testing whether the Θ threshold has been exceeded. In other words, any adversarial strategy that lets any of the nodes $p_{n-t}^{byz}, \dots, p_{n-1}^{byz}$ to complete more roundtrips with p_i than the nodes p_1, \dots, p_t cannot cause a ‘haste’ muteness detection of a correct node. For example in Fig. 3, the proposed muteness detector of node p_i would have ignored p_j ’s fake replies, and thus, never falsely suspects p_k and p_l .

Implementation. As shown in Fig. 4, Algorithm 5 does not send independent messages as it merely provides three interface functions to Algorithm 4, *i.e.*, $invoc()$, $rtComp(j)$, and $trusted()$. The algorithm’s state is based on the array $rt[][]$ (line 71), which stores the number of roundtrips that node p_i has completed with p_j . Note that $rt[][]$ counts separately the number of roundtrips p_i and p_k can complete during any period in which p_i and p_j are attempting to complete a single roundtrip.

The function $invoc()$ (line 73) nullifies $rt[][]$. We require p_i , upon completion of a roundtrip with p_j , to call $rtComp(j)$ (lines 74 to 76). It increments, for every $p_k \in \mathcal{P} \setminus \{p_i, p_j\}$, the counter in $rt_i[k][j]$ (line 75). Then, $rtComp(j)$ assigns zero

Algorithm 5: Class $\diamond P_{mute}$ detector; code for p_i .

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70 constants:  $B$ : a predefined bound on the integer size, say,  $2^{64} - 1$ .
71 variables:  $rt[\mathcal{P} \setminus \{p_i\}][\mathcal{P} \setminus \{p_i\}]$ : roundtrip counters, initially all entries are zero;
72 interface functions:
73  $invoc()$  do  $\{rt \leftarrow [[0, \dots, 0], \dots, [0, \dots, 0]]\}$ ;
74  $rtComp(j)$  begin
75   foreach  $p_k \in \mathcal{P} \setminus \{p_i, p_j\}$  do  $rt[k][j] \leftarrow \min\{B, rt[k][j] + 1\}$ ;
76    $rt[j] \leftarrow [0, \dots, 0]$ ;
77  $trusted()$  do return  $\{p_j : \Theta > \sum_{x \in \text{withoutTopItems}(t, j)} x\}$  where  $\{rt[j][\ell]\}_{p_\ell \in \mathcal{P}}$  is a multi-set with all the values in  $rt[j][\ell]$  and  $\text{withoutTopItems}(t, j)$  is the same multi-set after the removal of the top  $t$  values;

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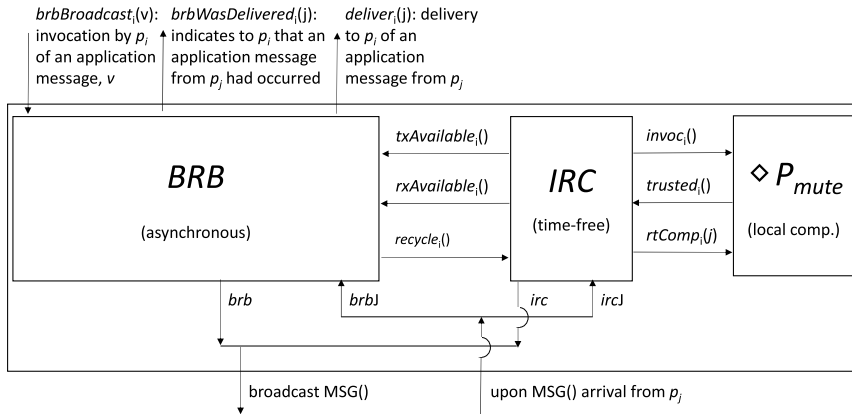


Fig. 4. Integrating BRB (§ 4) as well as IRC and $\diamond P_{mute}$ (§ 6).

to every entry in $rt_i[j]$ (line 76). The function $trusted()$ returns the set of unsuspected nodes. Its implementation relies on Assumption 6.2, which answers the above challenge of speculative acknowledgments. As a defense against the above attacks, p_i ignores the top t roundtrip counters when testing whether the Θ threshold has been exceeded.

Assumption 6.2. Let R be an execution, $p_i \in \mathcal{P}$ a correct node that repeatedly broadcasts $m(s) : s \in \mathbb{Z}^+$ and completes an unbounded number of roundtrips with every correct node. Let $rt_{i,c} : \mathcal{P} \times \mathcal{P} \rightarrow \mathbb{Z}^+$ be a mapping of $p_j, p_k \in \mathcal{P}$ with the number of roundtrips that p_i has completed with p_k between $c' \in R$ and $c \in R$, where c' is the first system state that immediately follows the last time p_i has completed a roundtrip with p_j , or the start of R (in case p_i has not completed any roundtrip with p_j between R 's start and c). Let $\sum_{x \in \text{withoutTopItems}_{i,c}(t, j)} x$ be the total number of roundtrips that p_i has completed until c when excluding the top t values of $rt_{i,c}$ of nodes that have completed with p_i the greatest number of roundtrips. We assume that if $\Theta \leq \sum_{x \in \text{withoutTopItems}_{i,c}(t, j)} x$ then p_j is mute to p_i w.r.t. $m(s)$, where Θ is a predefined constant.

7. Correctness of Algorithm 4 and Algorithm 5

Our correctness proof shows Convergence (Theorem 7.1) and Closure (Theorem 7.21). Theorem 7.1 demonstrates that the system completes the first part of the Convergence within $\mathcal{O}(\Theta + \text{channelCapacity} \cdot \lambda)$ asynchronous cycles assuming execution fairness among the correct nodes, as explained in § 2. It does so by showing that the system reaches a consistent system state, which Definition 7.1 specifies. This proof also includes the demonstration, in Claims 7.6 and 7.7, that Algorithm 5 satisfies $\diamond P_{mute}$'s requirements (Definition 6.2). Once Convergence is done, no execution fairness is needed when demonstrating Closure. This is done in Theorem 7.21, which shows that Algorithms 4 and 5 satisfy the requirements of the IRC task (Definition 6.1). Theorem 7.21's proof uses Theorem 7.11, which states that IRC-completion and $\diamond P_{mute}$'s requirements can be demonstrated using similar arguments to the ones in Theorem 7.1 while assuming a consistent starting state (and without assuming execution fairness).

Definition 7.1 (Consistent system states for the IRC task in Definition 6.1). Let R be an execution of Algorithm 4, $p_i, p_j \in \mathcal{P}$, and $c \in R$ be a system state of R . We say that c is consistent if the following holds.

1. $txLbL_i[j] = 0 \wedge cur_i[i] = (cur_j[i] + 1) \bmod (B + 1) \wedge cur_j[i] = (nxt_j[i] + 1) \bmod (B + 1)$ holds in c .
2. Given $c \in R$ and $s = cur_i[i]$, let $L_{c,i}(s)$ be all pairs (s, ℓ) in $MSG(-, (sj = s, -, \ell j = \ell, -))$ message in transit $channel_{i,j}$ and $MSG(-, (-, sj = s, -, \ell j = \ell))$ in $channel_{j,i}$ as well as p_i 's variables, $cur_i[i] = s$ and $txLbL_i[j] = \ell$, and p_j 's variables,

$cur_j[i] = s$ and $rxLbl_j[i]$ (line 59). $L_{c,i}(s)$ does not include values that were present in every system state between R 's starting state and c . I.e., every value in $L_{c,i}(s)$ was introduced (or reintroduced) between R 's start and c .

Theorem 7.1 (Convergence of Algorithms 4 and 5). *Let R be an execution of Algorithm 4 for which we assume fairness among the correct nodes, as explained in § 2. We also assume that Algorithm 4 uses the procedures presented in Algorithm 5 and that R satisfies Assumption 6.2. Let p_i be a correct node. Throughout R , suppose all correct nodes, p_j , IRC-fetch p_i 's counter, at least once in every $\mathcal{O}(1)$ asynchronous cycles, and p_i invokes IRC-increment, at least once in every $\mathcal{O}(1)$ asynchronous cycles. Within $\mathcal{O}(\Theta + \text{channelCapacity} \cdot \lambda)$ asynchronous cycles, the system reaches a consistent system state (Definition 7.1).*

Proof of Theorem 7.1. The proof is implied by Lemmas 7.2 to 7.10.

Lemma 7.2. *Within $\mathcal{O}(\Theta + \text{channelCapacity})$ asynchronous cycles, IRC-increment is enabled at least once, i.e., $\text{increment}_i()$ returns a non- \perp value.*

Proof of Lemma 7.2. Whenever p_i 's IRC-increment is enabled (lines 50 to 54), the return of a non- \perp value implies that the value of $cur_i[i]$ changes. For the case of $cur_i[i] = -1$, IRC-increment is clearly enabled (line 49). Thus, towards a contradiction, assume $cur_i[i] = s \geq 0$ holds in every system state of R' , where R' is a prefix of R that has at least $\mathcal{O}(\text{channelCapacity})$ asynchronous cycles.

Claims 7.3 to 7.8 show the needed contradiction since the latter claim implies the execution of line 52, which increments $cur_i[i]$.

Claim 7.3. *Within $\mathcal{O}(1)$ asynchronous cycles, the system reaches a state in which $\text{behind}_j(1, cur_j[i] = s, cur_j[i])$ holds.*

Proof of Claim 7.3. Within $\mathcal{O}(1)$ asynchronous cycles, node p_i broadcasts $\text{MSG}(-, (cur_i[i] = s, \bullet))$ (line 66). Thus, p_j receives $\text{MSG}(-, (cur_i[i] = s, \bullet))$ within $\mathcal{O}(1)$ asynchronous cycles. In the system state that immediately follows this message arrival (lines 67 to 68), the condition $\text{behind}_j(1, s, cur_j[i])$ holds due to the if-statement and its assignment to $cur_j[i]$ in line 64.

□Claim 7.3

Claim 7.4. *Within $\mathcal{O}(1)$ asynchronous cycles, the system reaches a state in which $\text{behind}_j(1, cur_j[i] = s, \text{next}_j[i])$ holds.*

Proof of Claim 7.4. By the assumption that p_j invokes $\text{fetch}_j(i)$ within $\mathcal{O}(1)$ asynchronous cycles, we know that the if-statement condition in line 56 eventually holds. Since otherwise the if-statement condition in line 56 does not hold and the assignment $\text{next}_j[i] \leftarrow cur_j[i]$ (line 57) occurs. □Claim 7.4

Claim 7.5. *Within $\mathcal{O}(1)$ asynchronous cycles, the system reaches a state in which the if-statement condition in line 60 holds w.r.t. p_i .*

Proof of Claim 7.5. By the proof of Claim 7.3, within $\mathcal{O}(1)$ asynchronous cycles, p_i sends $\text{MSG}(-, (cur_i[i] = s, -, txLbl_i[j] = \ell, -))$ to p_j and then p_j receives $\text{MSG}(-, (s, -, \ell, -))$ and assign ℓ to $rxLbl_j[i]$ (line 65) before replying with $\text{MSG}(-, (-, \text{next}_j[i] = s', -, \ell))$ (line 66). By Claims 7.3 and 7.4, we know that both $\text{behind}_j(1, cur_j[i] = s, cur_j[i])$ and $\text{behind}_j(1, cur_j[i] = s, \text{next}_j[i] = s')$ hold. By the assumption that $cur_i[i] = s$ holds throughout R' and the definition of $\text{behind}_j()$ (line 47), we know that $\text{behind}_j(2, cur_i[i] = s, \text{next}_j[i] = s')$ holds when p_j 's reply, $\text{MSG}(-, (-, \text{next}_j[i] = s', -, \ell))$, arrives at p_i . The rest of the proof is implied from lines 67 to 68 and then lines 59 to 60. □Claim 7.5

Claim 7.6. *Within $\mathcal{O}(\Theta)$ asynchronous cycles, R demonstrates the property of Strong Completeness (Definition 6.2).*

Proof of Claim 7.6. Let us consider the sequence of values of $rt_i[j'][k]$ in the different system states $c \in R$, where $p_i, p_{j'}, p_k \in \mathcal{P}$. Note that this sequence is defined by the function $rt_{i,c}(k, j')$ (Assumption 6.2). Thus, by line 77, we know that $j' \in \text{trusted}_i()$ if, and only if, $\Theta \leq \sum_{x \in \text{withoutTopItems}_{s_i,c}(t, j')} x$.

We demonstrate Strong Completeness in R by showing that, within $\mathcal{O}(\Theta)$ asynchronous cycles, every mute node, $p_{j'}$, is forever suspected w.r.t. round number $cur_i[i] = s$ (or the round number in $cur_i[i]$ becomes different than s).

Argument (1) *Within $\mathcal{O}(1)$ asynchronous cycles, p_i calls $\text{rtComp}_i(j')$ (line 61).*

By Claim 7.4, the if-statement condition in line 64 holds within $\mathcal{O}(1)$ asynchronous cycles. By Claim 7.5, line 61 is executed within $\mathcal{O}(1)$ asynchronous cycles.

Argument (2) *Within $\mathcal{O}(\Theta)$ asynchronous cycles, every mute node, $p_m \in \mathcal{P}$, is forever suspected (w.r.t. round number s) by every correct node (or the round number changes and stops being s). By Argument (1), p_i calls $\text{rtComp}_i(j')$ (line 61) within $\mathcal{O}(1)$ asynchronous cycles. I.e., for every correct node $p_k \in \mathcal{P} \setminus \{p_i, p_{j'}\}$, the value of $rt_i[k][j']$ is incremented, every $\mathcal{O}(1)$ asynchronous cycles. Thus, within $\mathcal{O}(\Theta)$ asynchronous cycles $p_{j'} \notin \text{trusted}_i()$ holds. □Claim 7.6*

Claim 7.7. Within $\mathcal{O}(\Theta)$ asynchronous cycles, R demonstrates the property of Eventual Strong Accuracy (Definition 6.2).

Proof of Claim 7.7. As in the proof of Claim 7.6, consider the sequence $rt_i[j'][k]$, which is defined by $rt_{i,c}(k, j')$ (Assumption 6.2). I.e., $j' \in \text{trusted}_i()$ if, and only if, $\Theta \leq \sum_{x \in \text{withoutTopItems}_{i,c}(t, j')} x$. We demonstrate Eventual Strong Accuracy in R by showing that, within $\mathcal{O}(\Theta)$ asynchronous cycles, the system reaches a state $c_\tau \in R$ in which every correct node, $p_{j'}$, is not suspected w.r.t. round number $cur_i[i] = s$ (or the round number in $cur_i[i]$ becomes different than s). By Argument (1) of Claim 7.6, since both p_i and $p_{j'}$ are correct, we know that p_i completes a roundtrip (that has a fresh label) with $p_{j'}$ within $\mathcal{O}(\Theta)$ asynchronous cycles. Whenever such a roundtrip is completed, p_i assigns $[0, \dots, 0]$ to $rt_i[j']$ (due to line 76) and the condition $\Theta > \sum_{x \in \text{withoutTopItems}_{i,c}(t, j')} x$ (line 77) holds until the next roundtrip completion (Assumption 6.2). $\square_{\text{Claim 7.7}}$

Claim 7.8. Suppose R satisfies $\diamond P_{\text{mute}}$'s requirements (Definition 6.2). Within $\mathcal{O}(\text{channelCapacity})$ asynchronous cycles, the system reaches a state in which the if-statement condition in line 49 does not hold w.r.t. p_i .

Proof of Claim 7.8. By the proof of Claim 7.3, within $\mathcal{O}(1)$ asynchronous cycles, p_i sends $\text{MSG}(-, (cur_i[i] = s, -, \text{txLbL}_i[j] = \ell, -))$ to every $p_j \in \mathcal{P}$. Then, p_j receives $\text{MSG}(-, (s, -, \ell, -))$ before assigning ℓ to $\text{rxLbL}_j[i]$ (line 65). By Claim 7.14, $\text{next}_j[i]$ stores the value s' for which $\text{behind}_j(1, s, s')$ holds eventually. Thus, p_j eventually send to p_i the message $\text{MSG}(-, (-, \text{next}_j[i] = s', -, \ell))$ (line 66). By Claim 7.5, the arrival of the latter message to p_i implies that p_i increments $\text{txLbL}_i[j]$ or that $\text{txLbL}_i[j] = B$ (line 62). Therefore, within $\mathcal{O}(\text{channelCapacity})$ asynchronous cycles, there is no trusted node $p_j \in \text{trusted}_i()$ for which $\text{txLbL}_i[j] \leq 2(\text{channelCapacity} + 1)$ holds. Thus, the if-statement condition in line 49 does not hold w.r.t. p_i . $\square_{\text{Claim 7.8}}$

$\square_{\text{Lemma 7.2}}$

Lemma 7.9. Within $\mathcal{O}(\Theta + \text{channelCapacity} \cdot \lambda)$ asynchronous cycles, p_i executes line 52, which causes p_j to execute line 64 and leads the system to a state in which for all nodes, $p_j \in \mathcal{P}$, it holds that $cur_j[i] = cur_i[i]$.

Proof of Lemma 7.9. Let us consider a suffix, R' , of execution R , of length $\mathcal{O}(\Theta + \text{channelCapacity} \cdot \lambda)$ asynchronous cycles, during which there is at least one node, $p_j \in \text{trusted}_i()$, for which $cur_j[i] \neq cur_i[i]$ holds throughout R' . From all the possible choices, let p_j to be a node for which, at the starting system state of R' , the value of $x_{i,j} = (cur_j[i] - cur_i[i]) \bmod (B + 1)$ is higher (or equal) to the value of $x_{i,k} = (cur_k[i] - cur_i[i]) \bmod (B + 1)$, for any $p_k \in \text{trusted}_i()$.

Argument (1) Only line 52 can introduce a new p_i 's round number value to the system, which p_j may assign to $cur_j[i]$. By the code of Algorithm 4, only lines 52 and 64 update the value of $cur[i]$. The former is at the sender-side, i.e., $cur_i[i]$, whereas the latter is at the receiver-side, i.e., $cur_j[i]$.

Argument (2) The assignment $cur_j[i] \leftarrow s$ occurs within $\mathcal{O}(\Theta + \text{channelCapacity} \cdot \lambda)$ asynchronous cycles, where s is the value of $cur_i[i]$ immediately after it was incremented. By Claim 7.8, within $\mathcal{O}(\text{channelCapacity})$ asynchronous cycles, p_i executes line 52, which increments the value of $cur_i[i]$, say, to s . Also, between any consecutive pair of such increments, p_j executes line 64 (see the proof of Claim 7.3), which tests whether $\neg \text{behind}(1, s, cur_j[i])$ holds, where s is a round number that arrived from p_i after it was assigned to $cur_i[i]$ in line 52. If this if-statement condition holds, the assignment $cur_j[i] \leftarrow s$ occurs. By $\text{behind}()$'s definition and the pigeonhole principle, there are at most λ increments to $cur_i[i]$ without reaching a state in which $\neg \text{behind}(1, s, cur_j[i])$ does not hold.

Argument (3) The claim holds. By Claim 7.8 and Argument (2), (i) $cur_i[i]$ is incremented to s within $\mathcal{O}(\Theta + \text{channelCapacity} \cdot \lambda)$ asynchronous cycles, (ii) the value s arrives to p_j , (iii) the value s is assigned to $cur_j[i]$. Due to Argument (1) and Assumption 6.1, (iv) $cur_j[i]$'s value does not change before a new assignment to $cur_i[i]$ occurs. Invariants (i) to (iv) imply the lemma. $\square_{\text{Lemma 7.9}}$

Lemma 7.10. Within $\mathcal{O}(\Theta + \text{channelCapacity} \cdot \lambda)$ asynchronous cycles, the system reaches a consistent state (Definition 7.1).

Proof of Lemma 7.10. We show that, within $\mathcal{O}(\Theta + \text{channelCapacity} \cdot \lambda)$ asynchronous cycles, invariants (1) and (2) hold. Within $\mathcal{O}(\Theta + \text{channelCapacity} \cdot \lambda)$ asynchronous cycles, p_i executes line 52, which causes p_j to execute line 64 and for $cur_j[i] = cur_i[i]$ to hold (Lemma 7.9). Let c be a system state that imminently follows the second time in which p_i executes line 52 in a way that leads to a state in which for all nodes, $p_j \in \mathcal{P}$, it holds that $cur_j[i] = cur_i[i]$.

Argument (1) Definition 7.1's Invariant (1) holds. In c , $\text{txLbL}_i[j] = 0$ holds due to the assignment in line 52. Also, by the proof of Lemma 7.9, such changes to the value of $cur_i[i]$, via increments by one module $B + 1$, introduce to the system state a round number that line 64 uses to update $cur_j[i]$. Imminently after such a change, $cur_i[i] = (cur_j[i] + 1) \bmod (B + 1)$ holds (Lemma 7.9). By the proof of Claim 7.4, the (first) change also leads to $cur_j[i] = \text{next}_j[i]$. Since c imminently follows the second change, both $cur_i[i] = (cur_j[i] + 1) \bmod (B + 1)$ and $cur_j[i] = (\text{next}_j[i] + 1) \bmod (B + 1)$ hold.

Argument (2) Definition 7.1's Invariant (2) holds. The proof is based on the one in [6, Ch. 4.2]. Due to Assumption 6.1, our proof is simpler. Let $c' \in R$. By Algorithm 4 and Definition 7.1, $L_{c',i}(s)$ cannot include more than $2(\text{channelCapacity} + 1)$ different pairs since (a) $\text{channel}_{i,j}$ and $\text{channel}_{j,i}$ include at most channelCapacity pairs each as well as (b) $cur_i[i]$ and $\text{txLbL}_i[j]$ are

the only variable in c that may contribute a unique set to $L_{c',i}(s)$, because ℓ_j (line 59) is an automatic (temporary) variable. Node p_i cannot change $cnt_i[i]$'s value twice before counting the reception of $2(\text{channelCapacity} + 1)$ different labels (line 49). Invariant (2) holds since during the period in which $\text{increment}_i()$ returns non- \perp values twice, the messages in the FIFO-channels (between p_i and p_j) and p_i 's variables, $cur_i[i]$ and $txLbl_i[i]$, do not hold values that have not changed since the starting system state of R' . $\square_{\text{Lemma 7.10}} \square_{\text{Theorem 7.1}}$

The proof of Theorem 7.11 is written using similar arguments to the ones in Theorem 7.1 without requiring fairness yet assuming that the system starts in a consistent state (Definition 7.1). This is done by letting the word 'eventually' substitute the parts in the proofs that quantify time in terms of asynchronous cycles, e.g., 'within $\mathcal{O}(1)$ asynchronous cycles'. We also note that when a correct node, p_i , sends, infinitely often, message m to another correct node, p_j , we know that m arrives at p_j infinitely often (due to the fair communication assumption, see § 2). Thus, this substitution satisfies the requirement to complete the roundtrips (cf. complete iterations in § 2) without requiring execution fairness. Similarly, it is also possible to show the satisfaction of Definition 6.2's requirements for $\diamond P_{mute}$'s class of muteness detectors. We bring hereafter the entire proof for the sake of completeness.

Theorem 7.11 (IRC-completion and Algorithm 5's Closure). *Let R be an Algorithm 4's execution that starts in a consistent system state (Definition 7.1). We also assume that Algorithm 4 uses the procedures presented in Algorithm 5 and that R satisfies Assumption 6.2. Let p_i be a correct node. Throughout R , suppose all correct nodes, p_j , IRC-fetch p_i 's counter, infinitely often, and p_i invokes IRC-increment, infinitely often. Eventually, R demonstrates IRC-completion (Definition 6.1). Also, R satisfies Definition 6.2's requirements for class $\diamond P_{mute}$ muteness detector.*

Proof of Theorem 7.11. The proof is implied by Lemmas 7.12 to 7.20.

Lemma 7.12. *Eventually, IRC-increment is enabled at least once, i.e., $\text{increment}_i()$ returns a non- \perp value.*

Proof of Lemma 7.12. Whenever p_i 's IRC-increment is enabled (lines 50 to 54), the return of a non- \perp value implies that the value of $cur_i[i]$ changes. For the case of $cur_i[i] = -1$, IRC-increment is clearly enabled (line 49). Thus, towards a contradiction, assume $cur_i[i] = s \geq 0$ holds in every system state of R' , where R' is a finite prefix of R . Claims 7.13 to 7.18 show the needed contradiction since the latter claim implies the execution of line 52, which increments $cur_i[i]$.

Claim 7.13. *Eventually $\text{behind}_j(1, cur_j[i] = s, cur_j[i])$ holds.*

Proof of Claim 7.13. Eventually, p_i broadcasts $\text{MSG}(-, (cur_i[i] = s, \bullet))$ (line 66). Thus, p_j receives $\text{MSG}(-, (cur_i[i] = s, \bullet))$ eventually. In the state that immediately follows this message's arrival (lines 67 to 68), the condition $\text{behind}_j(1, s, cur_j[i])$ holds due to the assignment to $cur_j[i]$ in line 64. $\square_{\text{Claim 7.13}}$

Claim 7.14. *Eventually, $\text{behind}_j(1, cur_j[i] = s, \text{next}_j[i])$ holds.*

Proof of Claim 7.14. By the assumption that p_j invokes $\text{fetch}_j(i)$ eventually, the if-statement in line 56 eventually holds due to line 57. $\square_{\text{Claim 7.14}}$

Claim 7.15. *Eventually, the if-statement condition in line 60 holds w.r.t. p_i .*

Proof of Claim 7.15. By Claim 7.13, eventually, p_i sends $\text{MSG}(-, (cur_i[i] = s, -, txLbl_i[j] = \ell, -))$ to p_j and then p_j receives $\text{MSG}(-, (s, -, \ell, -))$ before assigning ℓ to $rxLbl_j[i]$ (line 65). By Claim 7.14, $\text{next}_j[i]$ stores the value s' for which $\text{behind}_j(1, s, s')$ holds eventually. Thus, p_j eventually send to p_i the message $\text{MSG}(-, (-, \text{next}_j[i] = s', -, \ell))$ (line 66). By Claims 7.13 and 7.14, both $\text{behind}_j(1, cur_j[i] = s, cur_j[i])$ and $\text{behind}_j(1, cur_j[i] = s, \text{next}_j[i] = s')$ hold. By the assumption that $cur_i[i] = s$ holds throughout R' and the definition of $\text{behind}_j()$ (line 47), we know that $\text{behind}_j(2, cur_i[i] = s, \text{next}_j[i] = s')$ holds when p_j 's reply, $\text{MSG}(-, (-, \text{next}_j[i] = s', -, \ell))$, arrives at p_i . The rest of the proof is implied from lines 67 to 68 and then lines 59 to 60. $\square_{\text{Claim 7.15}}$

Claim 7.16. *Eventually, Strong Completeness (Definition 6.2) holds.*

Proof of Claim 7.16. As in Claim 7.6, consider the sequence $rt_i[j'][k]$. We demonstrate Strong Completeness in R by showing that, eventually, every mute node, $p_{j'}$, is forever suspected w.r.t. round $cur_i[i] = s$ (or $cur_i[i]$ becomes different than s).

Argument (1) *Eventually, p_i calls $\text{rtComp}_i(j')$ (line 61). The if-statement in line 60 holds eventually (Claim 7.15). Thus, line 61 is executed eventually.*

Argument (2) Eventually, every mute node, $p_m \in \mathcal{P}$, is forever suspected w.r.t. s by every correct node (or the round number changes). By Argument (1), p_i calls $\text{rtComp}_i(j')$ (line 61) eventually. I.e., every correct $p_k \in \mathcal{P} \setminus \{p_i, p_{j'}\}$, $\text{rt}_i[k][j']$ is incremented, infinitely often. Eventually $p_{j'} \notin \text{trusted}_i()$. $\square_{\text{Claim 7.16}}$

Claim 7.17. Eventually, Eventual Strong Accuracy (Definition 6.2) holds.

Proof of Claim 7.17. As in Claim 7.6, consider the sequence $\text{rt}_i[j'][k]$. We demonstrate Eventual Strong Accuracy in R by showing that, eventually, the system reaches $c_\tau \in R$ in which every correct $p_{j'}$ is not suspected w.r.t. $\text{cur}_i[i] = s$ (or $\text{cur}_i[i]$ becomes different than s). By Argument (1) of Claim 7.16, since both p_i and $p_{j'}$ are correct, we know that p_i completes a roundtrip (that has a fresh label) with $p_{j'}$ eventually. Whenever such a roundtrip is completed, p_i assigns $[0, \dots, 0]$ to $\text{rt}_i[j']$ (due to line 76) and $\Theta > \sum_{x \in \text{withoutTopItems}_i(t, j')} x$ (line 77) holds until the next roundtrip completion. $\square_{\text{Claim 7.16}}$

Claim 7.18. Suppose R satisfies $\diamond P_{\text{mute}}$'s requirements (Definition 6.2). Eventually, the if-statement condition in line 49 does not hold w.r.t. p_i .

Proof of Claim 7.18. By the proof of Claim 7.13, p_i sends $\text{MSG}(-, (\text{cur}_i[i] = s, -, \text{txLb}_i[j] = \ell, -))$ to every $p_j \in \mathcal{P}$ eventually. Then, node p_j receives $\text{MSG}(-, (s, -, \ell, -))$ before assigning ℓ to $\text{rxLb}_j[i]$ (line 65). By Claim 7.14, $\text{next}_j[i]$ stores the value s' for which $\text{behind}_j(1, s, s')$ holds eventually. Thus, p_j eventually send to p_i the message $\text{MSG}(-, (-, \text{next}_j[i] = s', -, \ell))$ (line 66). By Claim 7.15, the arrival of the latter message to p_i implies that p_i increments $\text{txLb}_i[j]$ or that $\text{txLb}_i[j] = B$ (line 62). Therefore, eventually, there is no $p_j \in \text{trusted}_i()$ for which $\text{txLb}_i[j] \leq 2(\text{channelCapacity} + 1)$ holds. Thus, the if-statement condition in line 49 does not hold w.r.t. p_i . $\square_{\text{Claim 7.18}} \square_{\text{Lemma 7.12}}$

Lemma 7.19. Eventually, p_i executes line 52, which causes p_j to execute line 64 and leads the system to a state in which for all nodes, $p_j \in \mathcal{P}$, it holds that $\text{cur}_j[i] = \text{cur}_i[i]$.

Proof of Lemma 7.19. Eventually, p_i executes line 52, which causes p_j to execute line 64 and for $\text{cur}_j[i] = \text{cur}_i[i]$ to hold (Lemma 7.19). Let c be a system state that imminently follows the second time in which p_i executes line 52 in a way that leads to a state in which for all nodes, $p_j \in \mathcal{P}$, it holds that $\text{cur}_j[i] = \text{cur}_i[i]$. The rest of the proof demonstrates that Definition 7.1's invariants (1) and (2) hold. The proof is identical to the ones of Claim 7.10's arguments (1) and (2). $\square_{\text{Lemma 7.19}}$

Lemma 7.20. Eventually, the system reaches a consistent state (Definition 7.1).

Proof of Lemma 7.20. Eventually, p_i executes line 52, which causes p_j to execute line 64 and for $\text{cur}_j[i] = \text{cur}_i[i]$ to hold (Lemma 7.19). Let c be a system state that imminently follows the second time in which p_i executes line 52 in a way that leads to a state in which for all nodes, $p_j \in \mathcal{P}$, it holds that $\text{cur}_j[i] = \text{cur}_i[i]$. The rest of the proof is identical to arguments (1) and (2) in Lemma 7.10. $\square_{\text{Lemma 7.20}} \square_{\text{Theorem 7.11}}$

Theorem 7.21 (Algorithm 4's Closure). Let $p_i \in \mathcal{P}$ be a correct node and R be an Algorithm 4's execution that starts in a consistent system state (Definition 7.1) as well as Algorithm 5 satisfies the requirements for class $\diamond P_{\text{mute}}$ muteness detector (Definition 6.2). Suppose all correct nodes, p_j , IRC-fetch p_i 's counter infinitely often and p_i invokes IRC-increment infinitely often. R demonstrates an IRC construction (Definition 6.1).

Proof of Theorem 7.21. IRC-completion is implied by Theorem 7.11.

IRC-validity. Suppose a correct node, p_j , IRC-fetches s in $a_j \in R$ from p_i 's counter. We show that p_i had IRC-incremented cnt_i to s in step a_i , which appears in R before a_j . Suppose, towards a contradiction, $\nexists a_i \in R$, yet a_j returns $s \neq \perp$ from $\text{fetch}_j(i)$ when executing line 57.

By the assumption that a_j returns $s \neq \perp$ from $\text{fetch}_j(i)$, we know that $\text{cur}_j[i] = s$ immediately before a_j (line 57). Node p_j assigns s to $\text{cur}_j[i] : i \neq j$ only in line 64 when it processes a message coming from the sender p_i . However, p_i can assign s to $\text{cur}_i[i]$ only at line 52, i.e., a_i exists.

IRC-preemption. Suppose that there is a correct node, $p_k \in \mathcal{P}$, that does not IRC-fetch s from p_i 's round counter during R even after a_i (in which p_i IRC-increment cnt_i to $s \neq -1$). Also, let a'_i be a step that appears in R after a_i and includes an IRC-increment invocation by p_i . We show that a'_i 's invocation returns \perp , i.e., a'_i 's invocation is disabled.

Since there is no step in which $\text{fetch}_k(i)$ returns s , it implies that $\text{next}_k[i] \neq s$ holds in any system state during R (line 57), when considering the last B p_i 's IRC-increments. Therefore, p_k does not send $\text{MSG}(-, (-, \text{next}[j] = s, \bullet))$ to p_i (line 66). This means that, as long as $\text{cur}_i[i] = s$, it holds that $\text{txLb}_i[j] = 0$. Also, as long as $\text{cur}_i[i] = s$, whenever p_i invokes $\text{increment}_i()$, the if-statement condition in line 49 holds. Thus, $\text{increment}_i()$ returns \perp in step a'_i .

IRC-integrity-1. Lines 56 to 57 and `behind()`'s definition imply that no correct node, p_i , can IRC-fetch the same value twice from the counter of the same node, say, p_j (when considering the last B IRC-increments done by p_i).

IRC-integrity-2. Suppose p_j IRC-fetches s' from p_i 's counter in step a'_j that appears in R after a_j (in which p_j IRC-fetches s). We show that p_i IRC-incremented cnt_i to s and then to s' . Step a'_j appears in R after a_j (IRC-preemption, -validity, and line 52) and a_j after a_i (IRC-validity). Since $s \neq s'$ (IRC-integrity-1), $a_i \neq a'_i$ holds. By line 52, s was IRC-incremented before s' when considering the B IRC-increments preceding a'_i . \square Theorem 7.21

8. Coordinating the recycling of BRB objects

We sketch an integration approach between the components proposed in this paper. Fig. 4 presents Algorithm 3's interface for recycling of BRB instances, *i.e.*, `recycle()`, `txAvailable()`, and `rxAvailable()` operations. (The interface between IRC and $\diamond P_{mute}$ is irrelevant to Algorithm 3.) The function `recyclei(k)` (line 6) lets the recycling mechanism reset `msgi[k][[]]`. The notation $f_i()$ says that p_i executes the function $f()$. For the single-instance BRB (without recycling), define `txAvailable()` and `rxAvailable(k)` (line 5) to return `True`. We redefine these implementations in § 6 as a preliminary integration approach between BRB and IRC via message piggybacking (lines 35 to 36).

The two SSBFT algorithms are integrated via specified interfaces and message piggybacking (Fig. 4). Thus, our SSBFT repeated BRB solution increases BT's message size only by a constant per BRB, but the number of messages per instance stays similar. The integrated solution can run an unbounded number of (concurrent and independent) BRB instances. The advantage is that the more communication-intensive component, *i.e.*, SSBFT BRB, is not associated with any synchrony assumptions. Specifically, one can run δ concurrent BRB instances, where δ is a parameter for balancing the trade-off between fault recovery time and the number of BRB instances that can be used (before the next δ concurrent instances can start). The above extension mitigates the effect of the fact that, for the repeated BRB problem, muteness detectors are used and mild synchrony assumptions are made to circumvent well-known impossibilities, *e.g.*, [38]. Those additional assumptions are required for the entire integrated solution to work. To the best of our knowledge, there is no proposal for a weaker set of assumptions for solving the studied problem in a self-stabilizing manner.

The above extension facilitates the implementation of FIFO-ordered delivery SSBFT repeated BRB. Here, each of the δ instances is associated with a unique label $\ell \in \{0, \dots, \delta - 1\}$. The implementation makes sure that no node p_i delivers a BRB message with label $\ell > 0$ before all the BRB messages with labels in $\{0, \dots, \ell - 1\}$. (For the case of $\ell = 0$, the delivery is unconditional.)

9. Conclusion

To the best of our knowledge, this paper presents the first SSBFT algorithms for IRC and repeated BRB (that follows Definition 1.1) for hybrid asynchronous/time-free systems. As in BT, the SSBFT BRB algorithm takes several communication rounds of $\mathcal{O}(n^2)$ messages per instance whereas the IRC algorithm takes $\mathcal{O}(n)$ messages but requires synchrony assumptions. We hope that the proposed solutions, *e.g.*, the proposed recycling mechanism and the hybrid composition of asynchronous/time-free system settings, will facilitate new SSBFT building blocks.

Declaration of competing interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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